

**Micro Commercial Components** 



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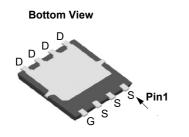
## Features

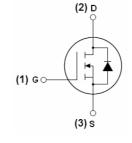
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Halogen free available upon request by adding suffix "-HF"
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1

#### Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter		Rating	Unit	
V <sub>DS</sub>	Drain-source Voltage		30	V	
I <sub>D</sub>	Drain Current-Continuous	T <sub>C</sub> = 25°C	30	А	
		T <sub>C</sub> = 100°C	21	~	
I <sub>DM</sub>	Pulsed Drain Current (Note 1)		60	А	
$V_{\text{GS}}$	Gate-source Voltage		±20	V	
PD	Maximum Power Dissipation		25	W	
$R_{thJC}$	Thermal Resistance, Junction-to-Case(Note 2)		5	°C/W	
Eas	Single pulse avalanche energy (Note 5)		70	mj	
TJ	Operating Junction Temperature		-55 to +150	°C	
T <sub>STG</sub>	Storage Temperature		-55 to +150	°C	

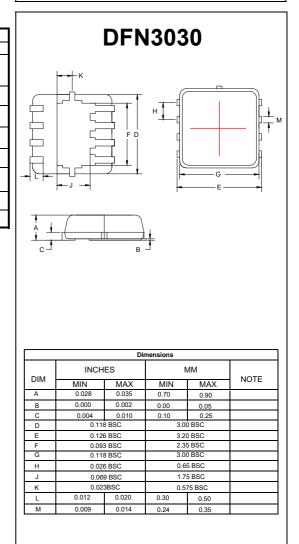
### EQUIVALENT CIRCUIT





## MCG30N03

## N-Channel Power MOSFET



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### ELECTRICAL CHARACTERISTICS(T<sub>a</sub>=25 $^{\circ}$ C unless otherwise specified)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·		•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	30	33	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	·					
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	1	1.5	2.3	V
Drain-Source On-State Resistance	D	V <sub>GS</sub> =10V, I <sub>D</sub> =10A	-	6.3	9	mΩ
	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	9.2	13	
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	15	-	-	S
Dynamic Characteristics (Note4)	·					
Input Capacitance	Clss	- V <sub>DS</sub> =15V,V <sub>GS</sub> =0V, F=1.0MHz	-	1490	-	PF
Output Capacitance	C <sub>oss</sub>		-	220	-	PF
Reverse Transfer Capacitance	Crss		-	135	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =15V,I <sub>D</sub> =10A V <sub>GS</sub> =10V,R <sub>GEN</sub> =1.8Ω	-	10	-	nS
Turn-on Rise Time	tr		-	8	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	30	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	5	-	nS
Total Gate Charge	Qg	- V <sub>DS</sub> =15V,I <sub>D</sub> =9A, - V <sub>GS</sub> =10V	-	15	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	4.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =10A	-	0.85	1.2	V
Diode Forward Current (Note 2)	Is		-	-	25	А
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 10A	-	22	35	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs(Note3)	-	12	20	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

#### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

**2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.

**3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.

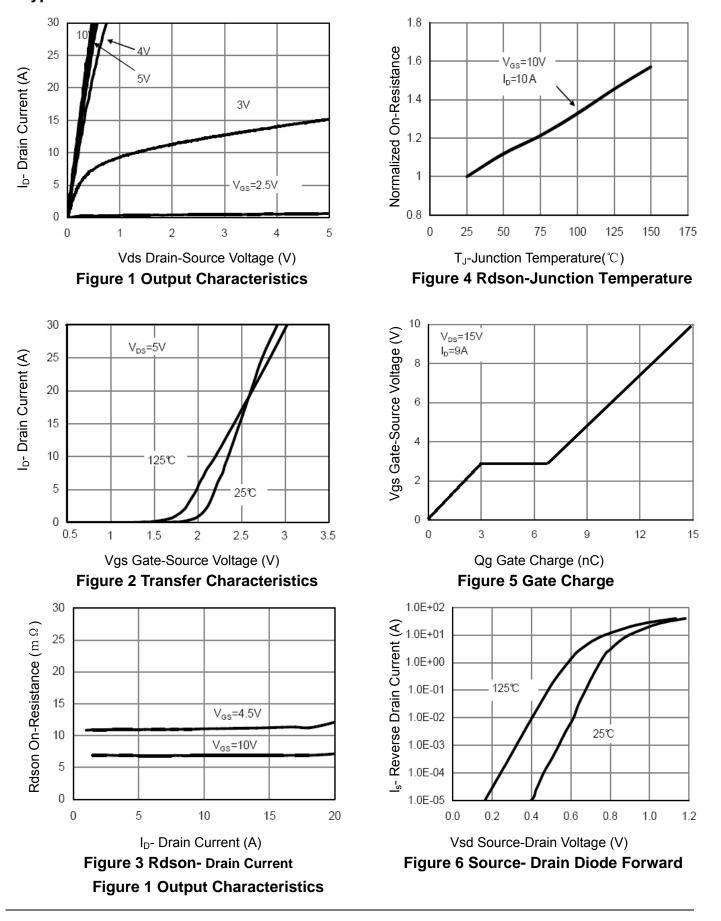
4. Guaranteed by design, not subject to production

5. EAS condition: Tj=25  $^\circ C$  ,V\_DD=15V,V\_G=10V,L=0.1mH,Rg=25 $\Omega$ 

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### **Typical Electrical and Thermal Characteristics**

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50

50

0

PD

1

100

V<sub>DS</sub>=V<sub>GS</sub>

100

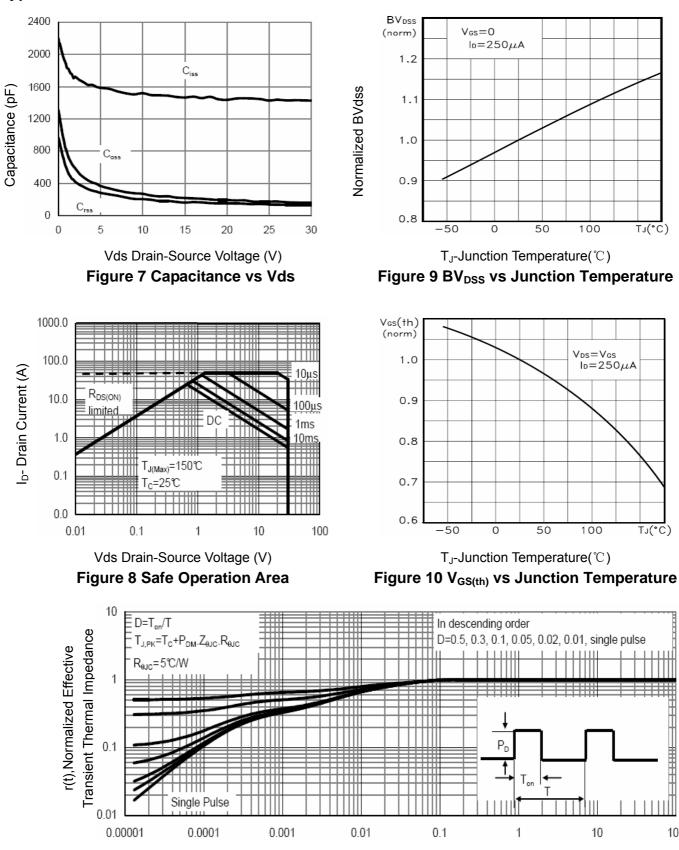
10

(°C) TJ

ID=250μA

TJ(°C)

0



## **Typical Electrical and Thermal Characteristics**

Square Wave Pluse Duration(sec) Figure 11 Normalized Maximum Transient Thermal Impedance

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100



## **Ordering Information :**

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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