

## 20V(D-S) Dual N-Channel Enhancement Mode Power MOS FET

**General Features**

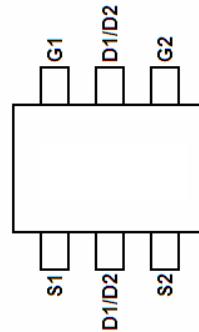
- $V_{DS} = 20V, I_D = 7A$
- $R_{DS(ON)} < 27m\Omega @ V_{GS}=2.5V$
- $R_{DS(ON)} < 21m\Omega @ V_{GS}=4.5V$
- ESD Rating: 2000V HBM
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package
- ESD protected

**Application**

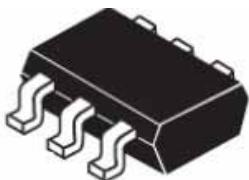
- PWM application
- Load switch



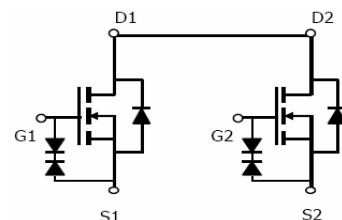
Lead Free



Marking and pin assignment

**PIN Configuration**

SOT-23-6L top view



Schematic diagram

**Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MSC0207SE	MSC0207SE	SOT-23-6L	Ø330mm	12mm	3000 units

**Absolute Maximum Ratings ( $T_A=25^\circ C$  unless otherwise noted)**

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$I_D$	7	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	30	A
Maximum Power Dissipation	$P_D$	1.25	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

**Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	100	°C/W
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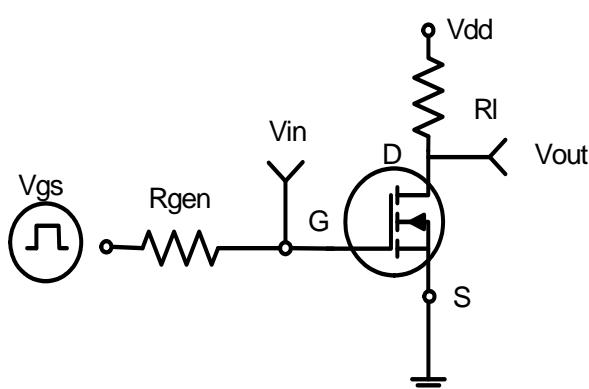
**Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	20		-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 10\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 10$	$\mu\text{A}$
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.55	0.7	0.95	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=6.5\text{A}$	-	15	21	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=5.5\text{A}$	-	20	27	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=7\text{A}$	-	20	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	1150	-	PF
Output Capacitance	$C_{\text{oss}}$		-	185	-	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	145	-	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=10\text{V}, R_{\text{L}}=1.35\Omega$ $V_{\text{GS}}=5\text{V}, R_{\text{GEN}}=3\Omega$	-	6		nS
Turn-on Rise Time	$t_r$		-	13		nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	52		nS
Turn-Off Fall Time	$t_f$		-	16		nS
Total Gate Charge	$Q_g$	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=7\text{A}, V_{\text{GS}}=4.5\text{V}$	-	15		nC
Gate-Source Charge	$Q_{\text{gs}}$		-	0.8	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	3.2	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=1\text{A}$	-	-	1.2	V
Diode Forward Current <sup>(Note 2)</sup>	$I_{\text{s}}$		-	-	7	A

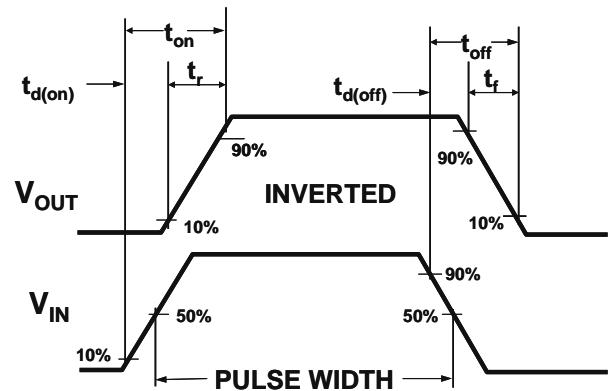
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

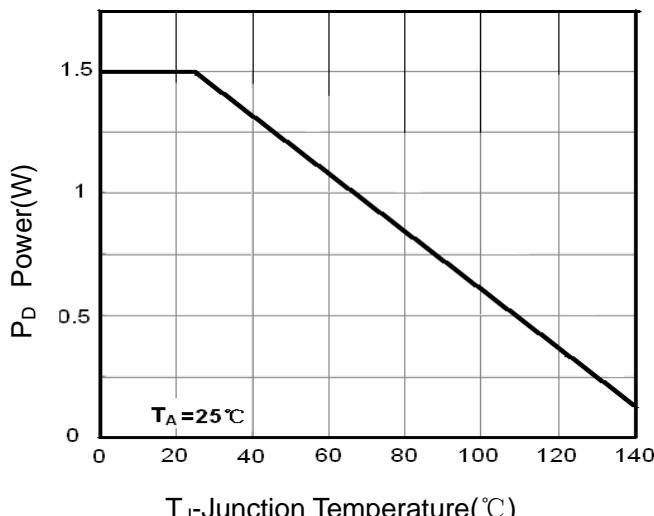
### Typical Electrical and Thermal Characteristics



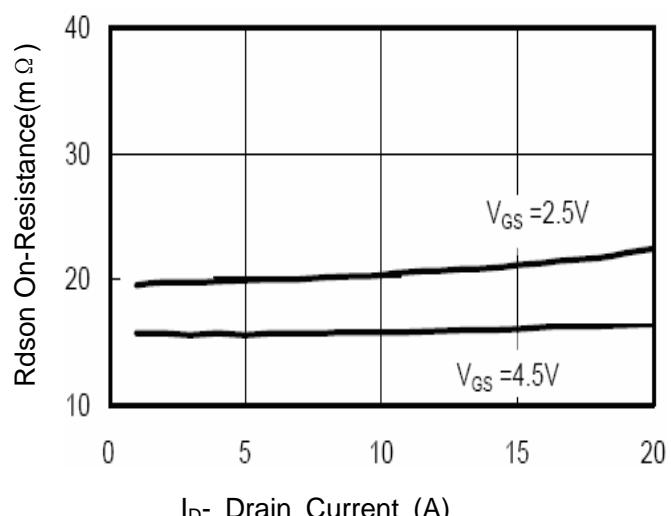
**Figure 1:Switching Test Circuit**



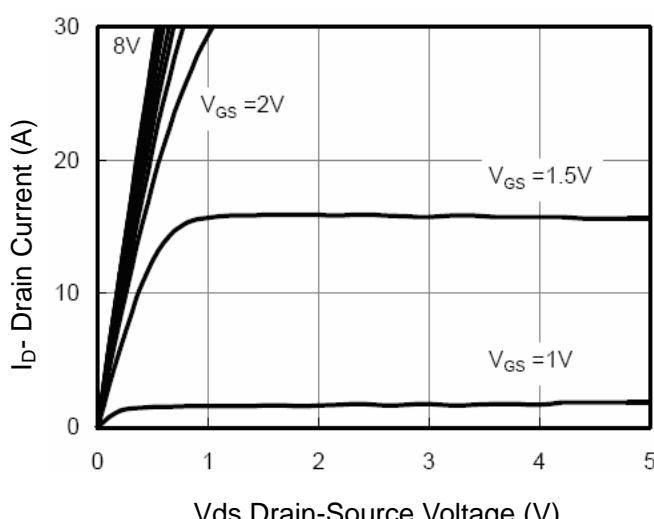
**Figure 2:Switching Waveforms**



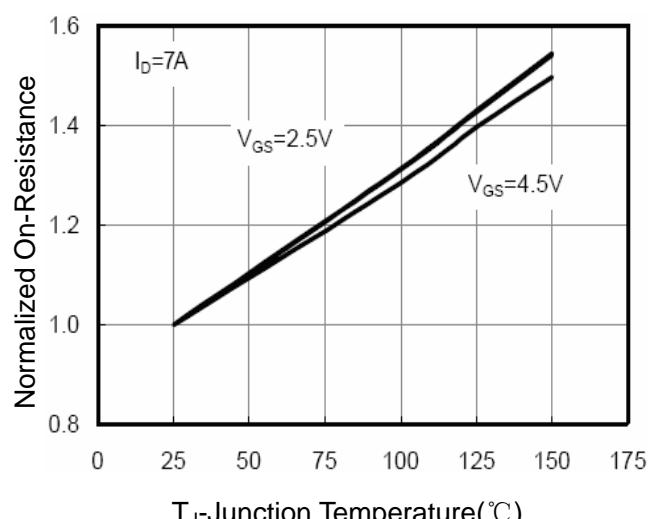
**Figure 3 Power Dissipation**



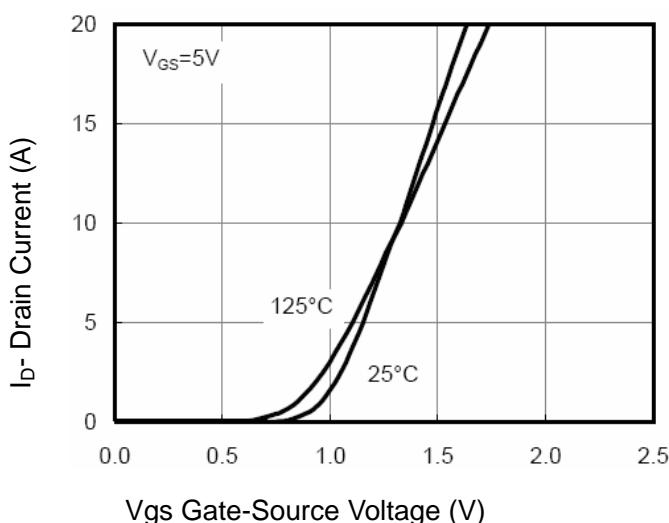
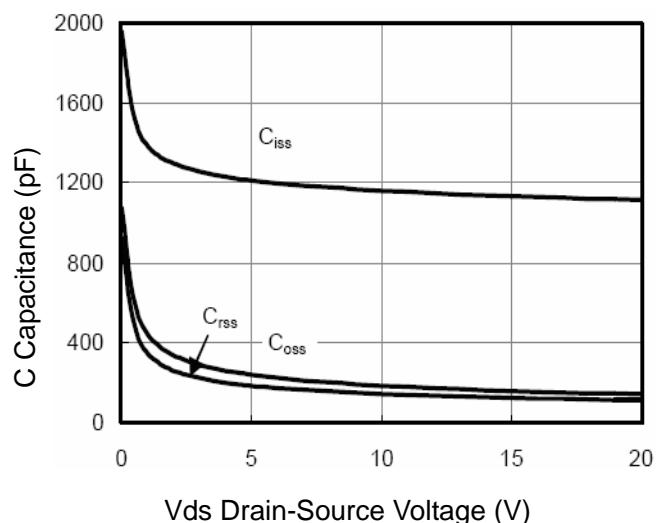
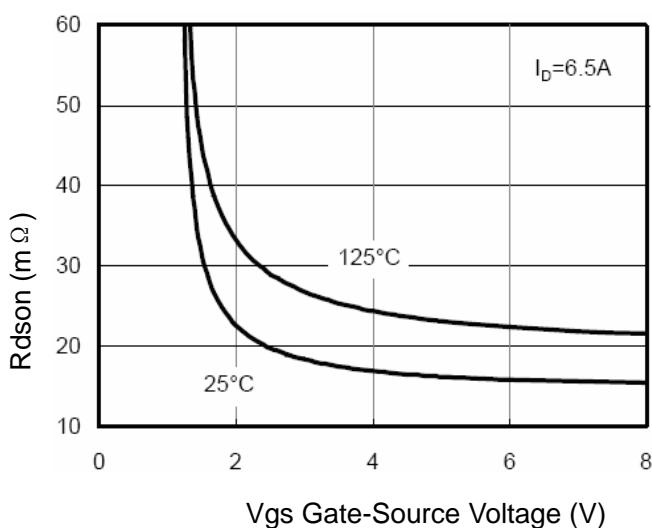
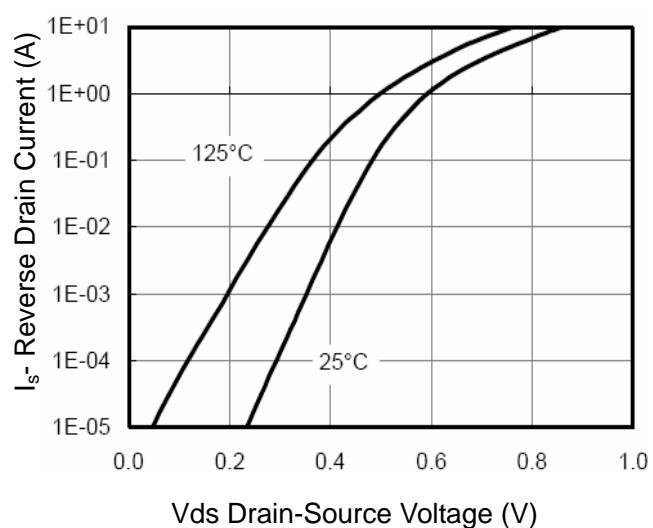
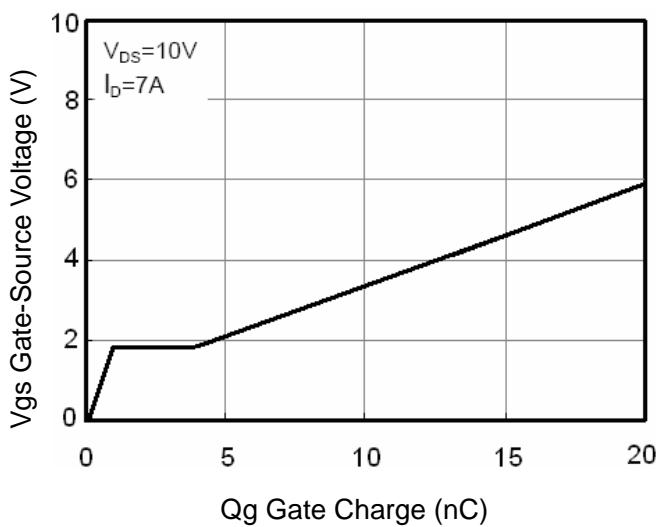
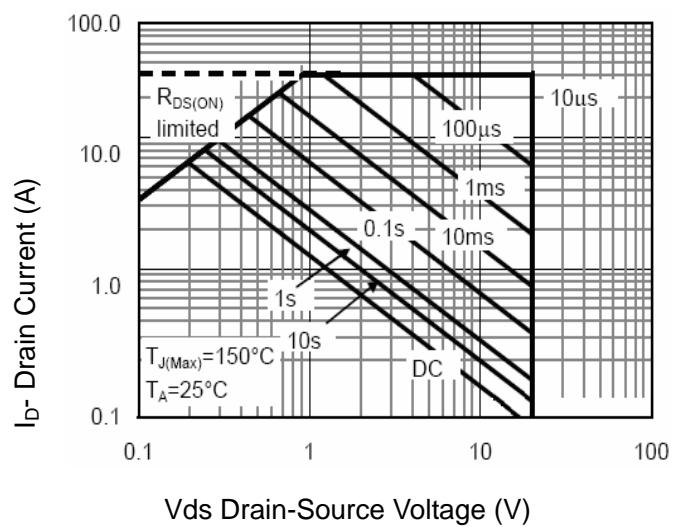
**Figure 6 Drain-Source On-Resistance**

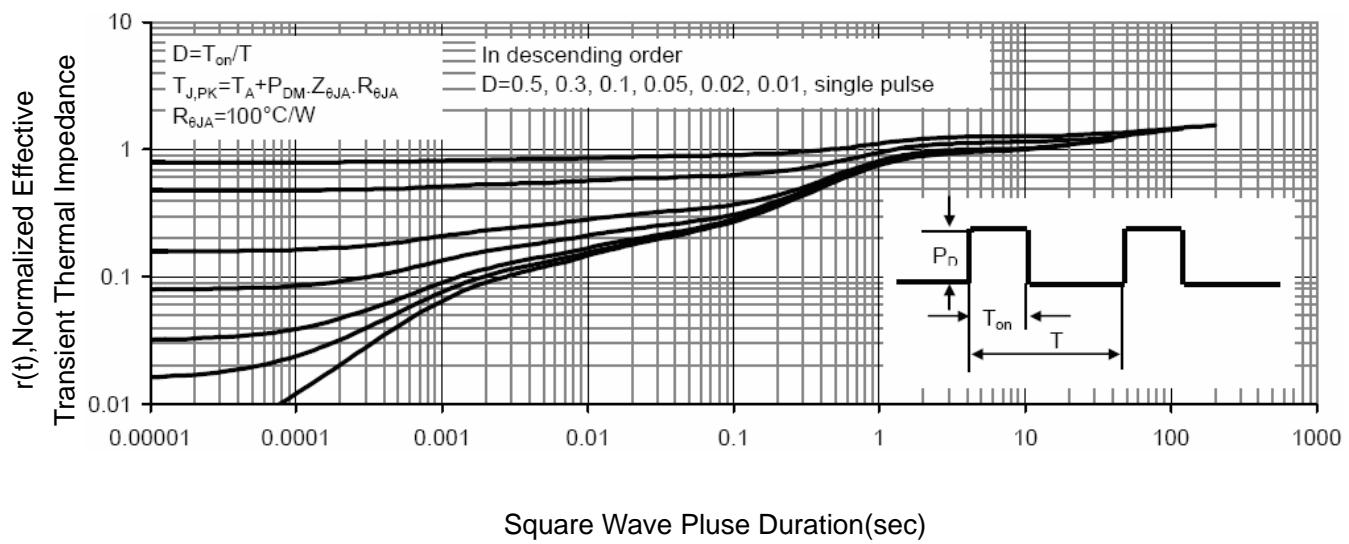


**Figure 5 Output Characteristics**

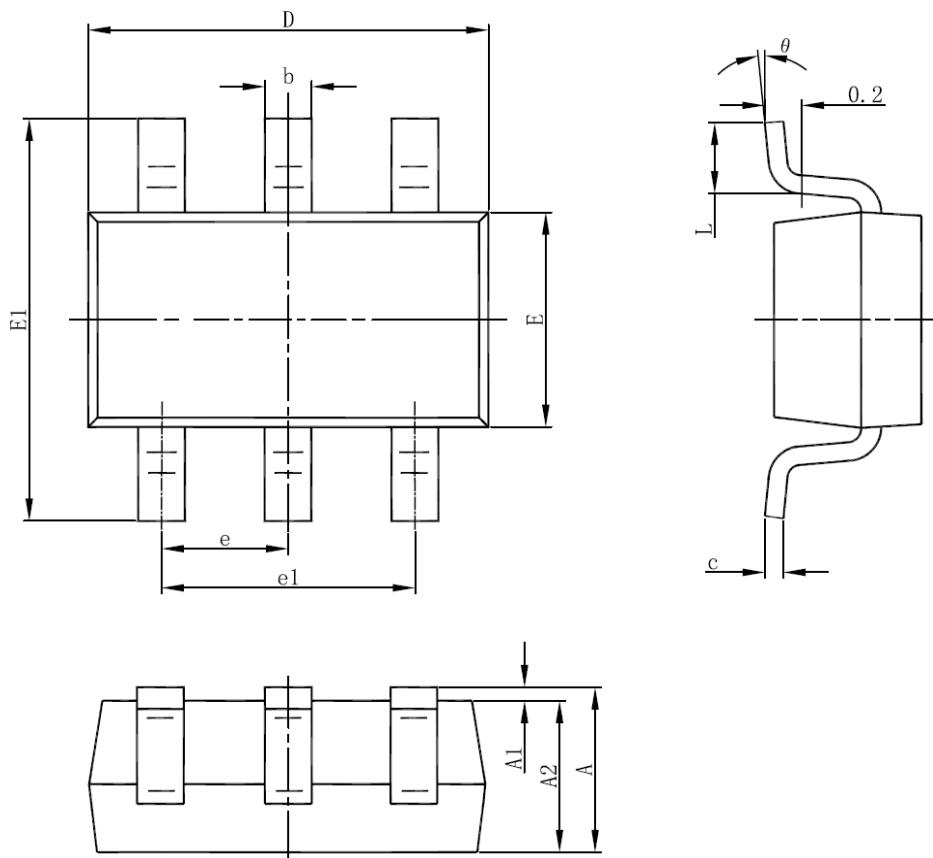


**Figure 8 Drain-Source On-Resistance**

**Figure 7 Transfer Characteristics****Figure 8 Capacitance vs  $V_{DS}$** **Figure 9  $R_{DSON}$  vs  $V_{GS}$** **Figure 10 Capacitance vs  $V_{DS}$** **Figure 11 Gate Charge****Figure 13 Safe Operation Area**



## SOT-23-6L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°