



Parameter	Rating	Units
Maximum Turn-On/Turn-Off Times	50	μs
Blocking Voltage	300	V_P
Load Current	50	$\text{mA}_{\text{rms}} / \text{mA}_{\text{DC}}$
On-Resistance (max)	100	Ω

Features

- Fastest Switching OptoMOS Relay, 50 μs
- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- FCC Compatible
- No EMI/RFI Generation
- Small 6-Pin DIP Package
- Machine Insertable, Wave Solderable
- Surface Mount Tape & Reel Version Available
- Flammability Classification Rating of V-0

Applications

- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security Systems
- Aerospace
- Industrial Controls
- Reed Relay Replacement

Description

PLA160 is a 300V, 50mA, 100 Ω 1-Form-A relay. This performance leader features the fastest switching speed (50 μs) available in an OptoMOS relay.

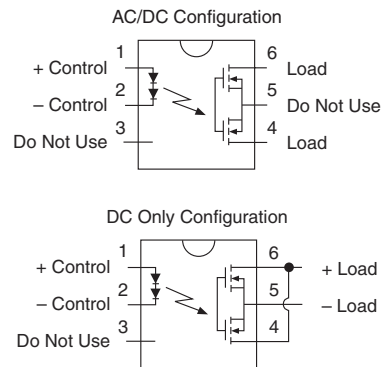
Approvals

- UL Recognized Component: File # E76270
- CSA Certified Component: Certificate # 1175739
- EN/IEC 60950 Certified Component: TUV Certificate B 13 12 82667 003

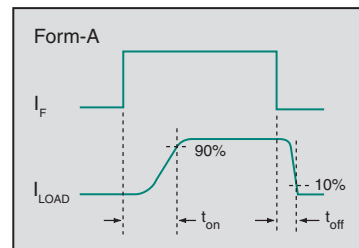
Ordering Information

Part Number	Description
PLA160	6-Pin DIP (50/Tube)
PLA160S	6-Pin Surface Mount (50/Tube)
PLA160STR	6-Pin Surface Mount (1,000/Reel)

Pin Configuration



Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	300	V _p
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

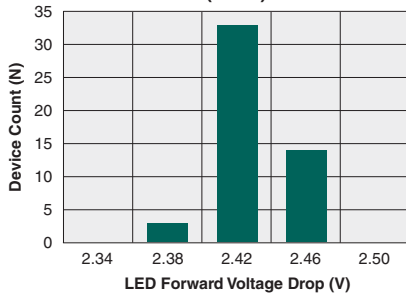
Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical Characteristics @ 25°C

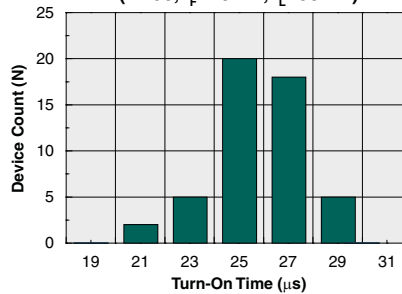
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current (Continuous)						
AC/DC Configuration	-	I _L	-	-	50	mA _{rms} / mA _{DC}
DC Configuration	-	I _L	-	-	80	mA _{DC}
Peak Load Current	t=10ms	I _{LPK}	-	-	±200	mA _p
On-Resistance						
AC/DC Configuration	I _F =50mA	R _{ON}	-	60	100	Ω
DC Configuration	I _F =80mA	R _{ON}	-	15	30	
Off-State Leakage Current						
	V _L =300V	I _{LEAK}	-	-	25	nA
	V _L =100V		-	1	10	
Switching Speeds						
Turn-On	I _F =10mA, V _L =10V	t _{on}	-	25	50	μs
Turn-Off		t _{off}	-	42		
Output Capacitance	50V, f=1MHz	C _{OUT}	-	3	-	pF
Input Characteristics						
Input Control Current to Activate	I _L =50mA	I _F	-	1.35	10	mA
Input Control Current to Deactivate	-	I _F	0.4	1.25	-	mA
Input Voltage Drop	I _F =10mA	V _F	1.8	2.4	2.8	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF

PERFORMANCE DATA @ 25°C (Unless Otherwise Noted) *

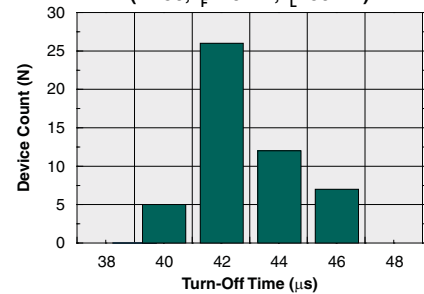
Typical LED Forward Voltage Drop (N=50)



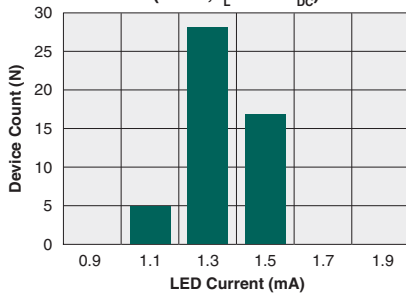
Turn-On Time Distribution (N=50, I_F=10mA, I_L=50mA)



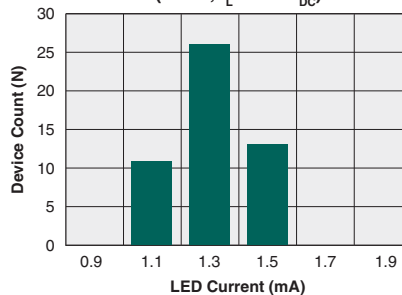
Turn-Off Time Distribution (N=50, I_F=10mA, I_L=50mA)



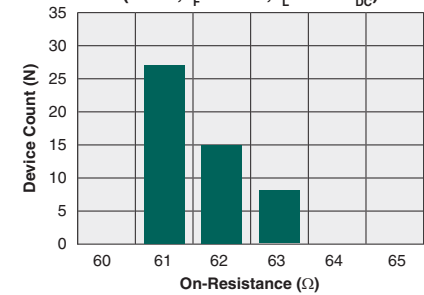
Typical I_F for Switch Operation (N=50, I_L=50mA_{DC})



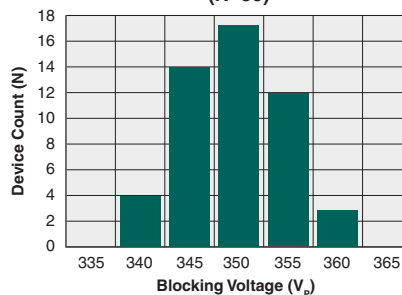
Typical I_F for Switch Dropout (N=50, I_L=50mA_{DC})



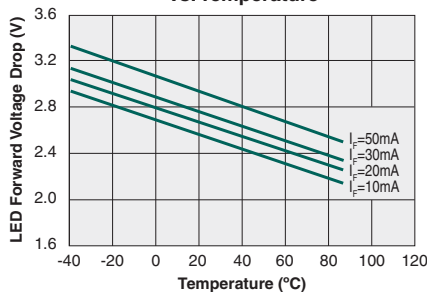
Typical On-Resistance Distribution (N=50, I_F=10mA, I_L=50mA_{DC})



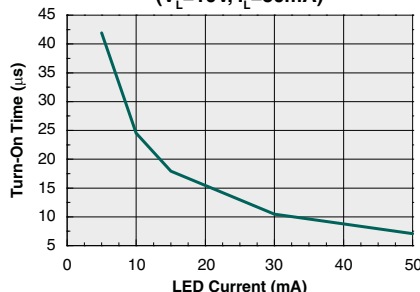
Typical Blocking Voltage Distribution (N=50)



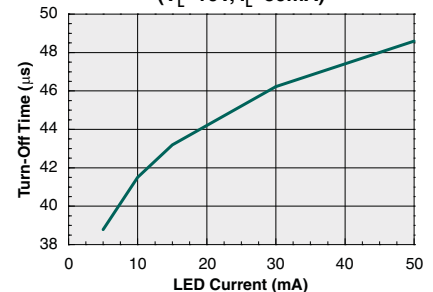
Typical LED Forward Voltage Drop vs. Temperature



Typical Turn-On Time vs. LED Forward Current (V_L=10V, I_L=50mA)

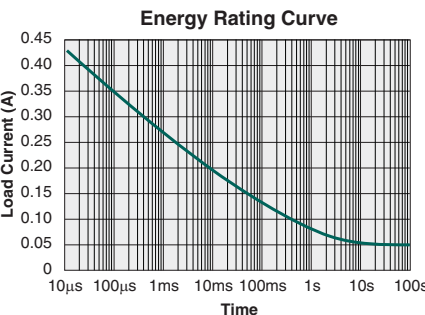
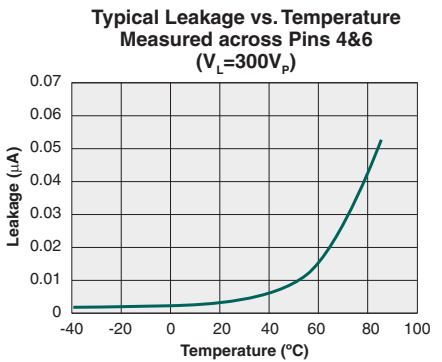
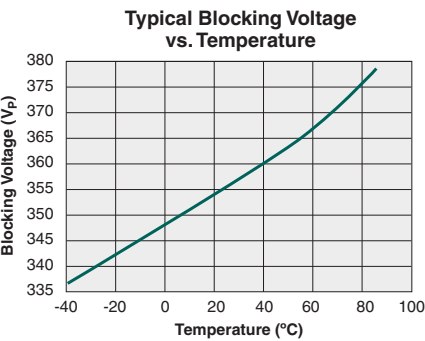
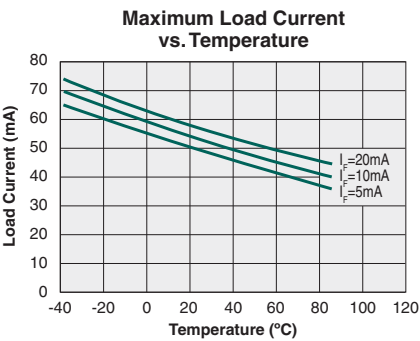
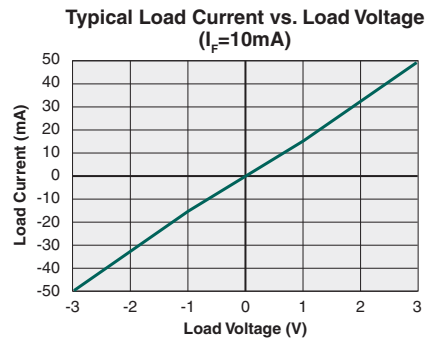
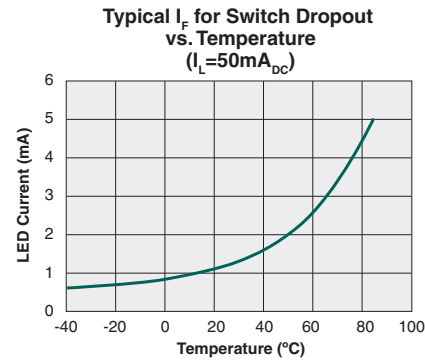
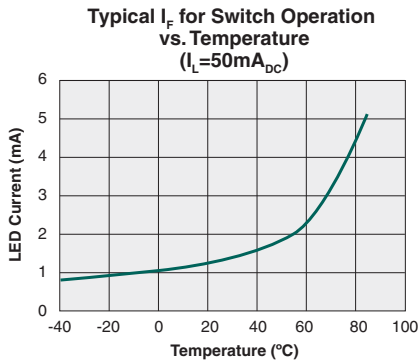
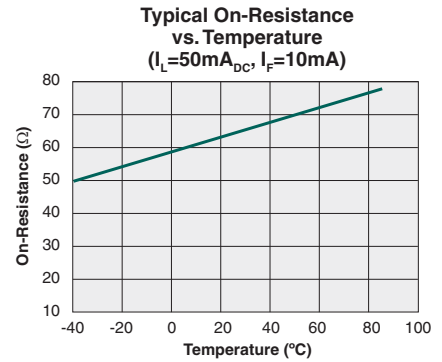
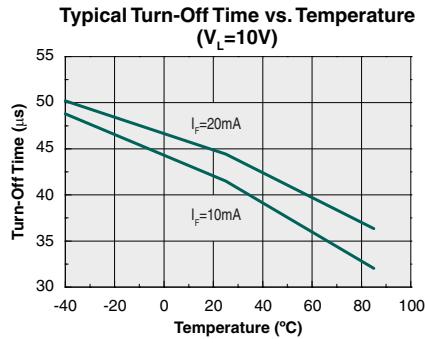
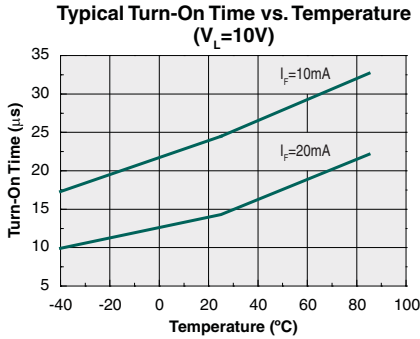


Typical Turn-Off Time vs. LED Forward Current (V_L=10V, I_L=50mA)



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA @ 25°C (Unless Otherwise Noted) *



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
PLA160 / PLA160S	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

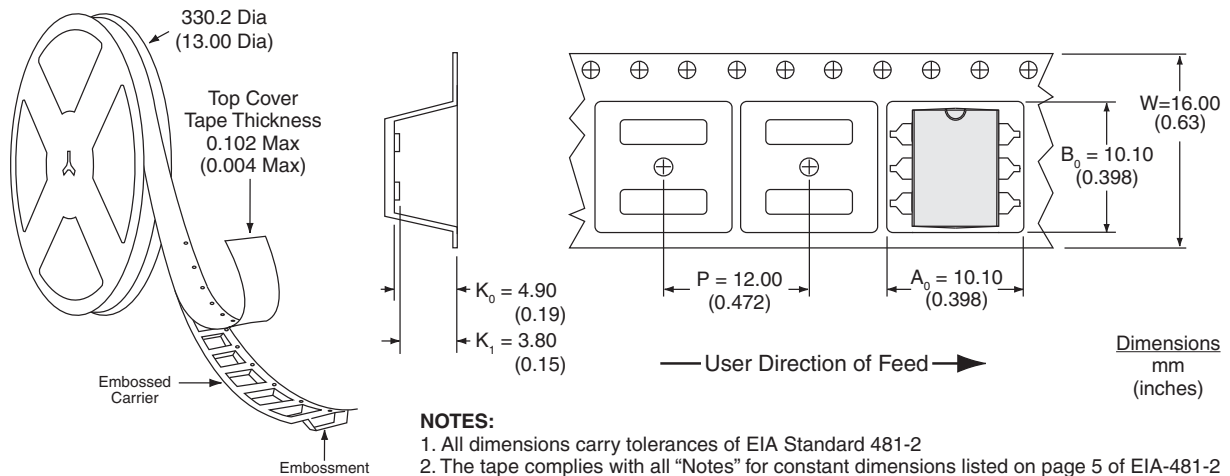
Device	Maximum Temperature x Time
PLA160 / PLA160S	250°C for 30 seconds

Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.



PLA160STR Tape & Reel



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