FRED Ultrafast Soft Recovery Diode 2 x 15A / 400V

FEATURES

- Ultrafast recovery time
- Very low leakage current
- Very low Q_{rr}
- Specified at operating conditions
- Designed and qualified for industrial level
- Planar FRED Chip
- 175°C operating junction temperature

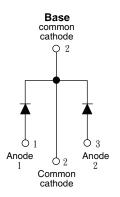
BENEFITS

- Reduced RFI and EMI
- Reduced power loss in diode and switching transistor
- Higher frequency operation
- · Reduced snubbing
- Reduced parts count



HFA30PA40C is a state of the art center tap ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 400V and 15A per leg continuous current, the HFA30PA40C is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the FRED product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the tb portion of recovery. The FRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These FRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The FRED HFA30PA40C is ideally suited for applications in power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.





PRODUCT SUMMARY				
V _R	400 V			
V _F at 15A at 25 °C	1.3 V			
I _{F(AV)}	2 x 15 A			
t _{rr} (typical)	19 ns			
T _J (maximum)	175 °C			
Q _{rr} (typical)	60 nC			
I _{RRM} (typical)	3.0 A			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Cathode to anode voltage	V _R		400	V			
Maximum continuous forward current per leg	- I _F	T _C = 140 °C	15				
per device			30	A			
Single pulse forward current	I _{FSM}		150				
Maximum repetitive forward current	I _{FRM}	T _C = 140 °C, square wave, 20KHz	30				
Operating junction and storage temperature range	T _J , T _{Stg}		- 55 to + 175	°C			



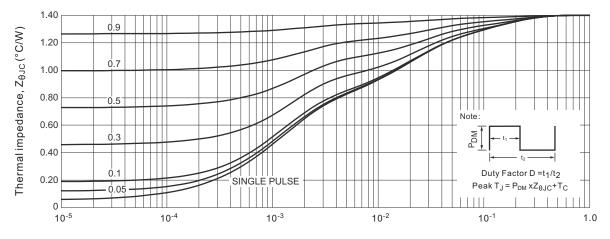
ELECTRICAL SPECIFIC	ICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	400	-	-	
Maximum forward voltage V _{FM}		I _F = 15 A	-	1.15	1.3	V
	V_{FM}	I _F = 30 A	-	-	1.6	
		I _F = 15 A, T _J = 125 °C	-	1.0	-	
Maximum reverse I _{RM}	V _R = V _R rated	-	0.5	10		
	INM	$T_J = 125$ °C, $V_R = V_R$ rated	-	-	500	μΑ
Junction capacitance	Ст	V _R = 200V	-	33	50	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	12	-	nH

DYNAMIC RECOVERY CHARACTERISTICS PERLEG (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS			TYP.	MAX.	UNITS
Reverse recovery time	t _{rr}	I _F = 0.5A, I _R = 1.0A, I _{RR} = 0.25A (RG#1 CKT)		-	22	27	- ns
		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}, T_J = 25^{\circ}\text{C}$		-	19	-	
	t _{rr1}	T _J = 25 °C	I _F = 15A dI _F /dt = 200 A/μs V _R = 266 V	-	35	60	- 115
	t _{rr2}	T _J = 125 °C		-	95	120	
Peak recovery current	I _{RRM1}	T _J = 25 °C		-	3	6.0	A
	I _{RRM2}	T _J = 125 °C		-	6	10	
Reverse recovery charge	Q _{rr1}	T _J = 25 °C		-	60	180	nC
	Q _{rr2}	T _J = 125 °C		-	300	600	

THERMAL - MECHANICAL SPECIFICATIONS PER LEG							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Lead temperature	T _{lead}	0.063" from case (1.6 mm) for 10 s	-	-	300	°C	
Junction to case, single leg conduction	В		-	-	1.40		
Junction to case, both legs conducting	- R _{thJC}		-	-	0.70	12001	
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	40	K/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.25	-		
Weight			-	6.0	-	g	
			-	0.21	-	OZ.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf . cm (lbf . in)	
Marking device		Case style TO-247AB (JEDEC)	HFA30PA40C			•	

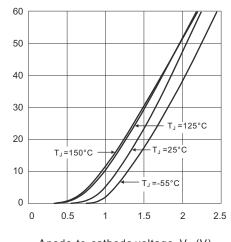


Fig.1 Maximum effective transient thermal impedance, junction-to-case vs. pulse duration



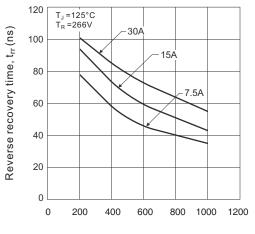
Rectangular pulse duration (seconds)

Fig.2 Forward current vs. forward voltage



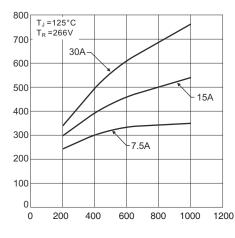
Anode-to-cathode voltage, $V_F(V)$

Fig.3 Typical reverse recovery time vs. current rate of change



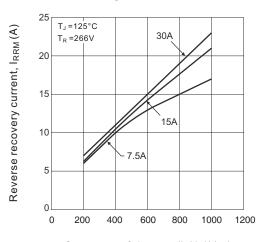
Current rate of change, -di_F/dt (A/µs)

Fig.4 Typical reverse recovery charge vs. current rate of change



Current rate of change, -di_F/dt (A/µs)

Fig 5. Typical reverse recovery current vs. current rate of change



Current rate of change, -di_F/dt (A/ μ s)

Forward current, I_F(A)

Reverse recovery charge, Qrr (nC)



Fig6. Dynamic parameters vs. junction temperature

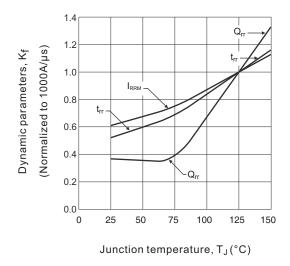


Fig.7 Maximum average forward current vs. case temperature

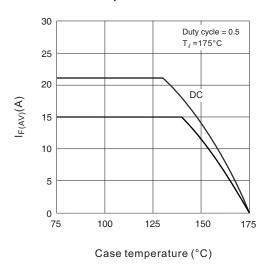
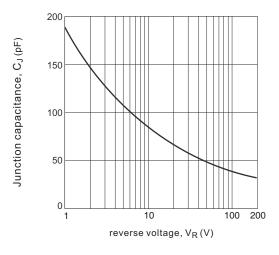


Fig.8 Junction capacitance vs. reverse voltage

Fig.9 Reverse recovery parameter test circuit



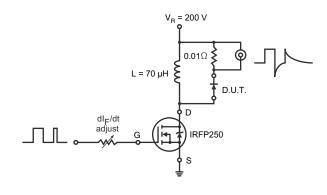
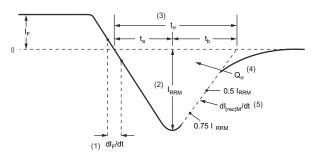


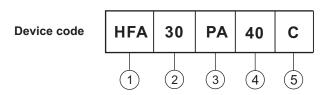
Fig.10 Reverse recovery waveform and definitions



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}
 - $Q_{rr} = \frac{t_{rr} I_{RRM}}{2}$
- (5) $dI_{(rec)M}/dt$ peak rate of change of current during t_b portion of t_{rr}



ORDERING INFORMATION TABLE



1 - FRED family

2 - Current rating (30 = 30 A, 15A x 2)

3 - Package outline (PA = TO-247, 3 pins)

4 - Voltage rating (40 = 400 V)

5 - Configuration (C = Center tap common cathode)

