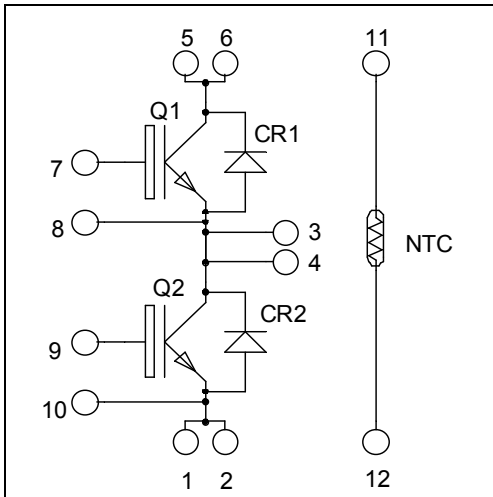


*Phase leg
High speed Trenchstop 5
IGBT Power Module*

**$V_{CES} = 650V$
 $I_C = 100A @ T_c = 25^\circ C$**


Application

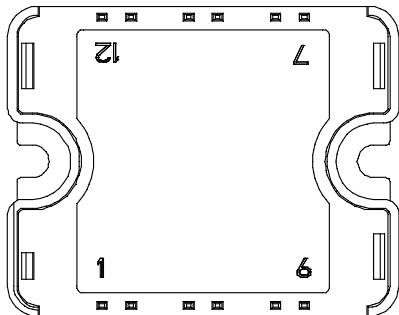
- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

- **Trench + Field Stop IGBT5 technology**
 - Low voltage drop
 - Low tail current
 - Switching frequency up to 100 kHz
 - Low leakage current
- Very low stray inductance
- Internal thermistor for temperature monitoring

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS compliant



Pins 1/2 ; 3/4 ; 5/6 must be shorted together

All ratings @ $T_j = 25^\circ C$ unless otherwise specified

Absolute maximum ratings (Per IGBT)

Symbol	Parameter	Max ratings	Unit
V_{CES}	Collector - Emitter Voltage	650	V
I_C	Continuous Collector Current	$T_C = 25^\circ C$	100
		$T_C = 80^\circ C$	60
I_{CM}	Pulsed Collector Current	$T_C = 25^\circ C$	200
V_{GE}	Gate - Emitter Voltage	± 20	V
P_D	Power Dissipation	250	W

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

Electrical Characteristics (per IGBT)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I_{CES}	Zero Gate Voltage Collector Current	$V_{GE} = 0V, V_{CE} = 650V$			100	μA
$V_{CE(sat)}$	Collector Emitter Saturation Voltage	$V_{GE} = 15V$ $I_C = 100A$		1.65 1.9	2.2	V
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 1mA$	3.3	4.0	4.7	V
I_{GES}	Gate – Emitter Leakage Current	$V_{GE} = 20V, V_{CE} = 0V$			240	nA

Dynamic Characteristics (per IGBT)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{ies}	Input Capacitance	$V_{GE} = 0V$		6000		pF
C_{oes}	Output Capacitance	$V_{CE} = 25V$		100		
C_{res}	Reverse Transfer Capacitance	$f = 1MHz$		22		
Q_G	Gate charge	$V_{GE} = 15V, I_C = 100A$ $V_{CE} = 520V$		240		nC
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (25°C) $V_{GE} = 15V$ $V_{Bus} = 400V$ $I_C = 50A$ $R_G = 2\Omega$		21		ns
T_r	Rise Time			15		
$T_{d(off)}$	Turn-off Delay Time			180		
T_f	Fall Time			18		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (150°C) $V_{GE} = 15V$ $V_{Bus} = 400V$ $I_C = 50A$ $R_G = 2\Omega$		20		ns
T_r	Rise Time			15		
$T_{d(off)}$	Turn-off Delay Time			205		
T_f	Fall Time			26		
E_{on}	Turn on Energy	$V_{GE} = 15V$ $V_{Bus} = 400V$		1.5		mJ
E_{off}	Turn off Energy	$I_C = 50A$ $R_G = 2\Omega$		0.6		
R_{Gint}	Integrated gate resistor			2.5		Ω
R_{thJC}	Junction to Case Thermal Resistance				0.6	$^{\circ}C/W$

Diode ratings and characteristics (per diode)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V_{RRM}	Peak Repetitive Reverse Voltage				650	V
I_{RM}	Reverse Leakage Current	$V_R = 650V$			100	μA
I_F	DC Forward Current			100		A
V_F	Diode Forward Voltage	$I_F = 100A$ $V_{GE} = 0V$		1.6 1.65	2.2	V
t_{rr}	Reverse Recovery Time	$I_F = 50A$ $V_R = 400V$ $di/dt = 3000A/\mu s$		46 62		ns
Q_{rr}	Reverse Recovery Charge			1 2		μC
R_{thJC}	Junction to Case Thermal Resistance				0.7	$^{\circ}C/W$

Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

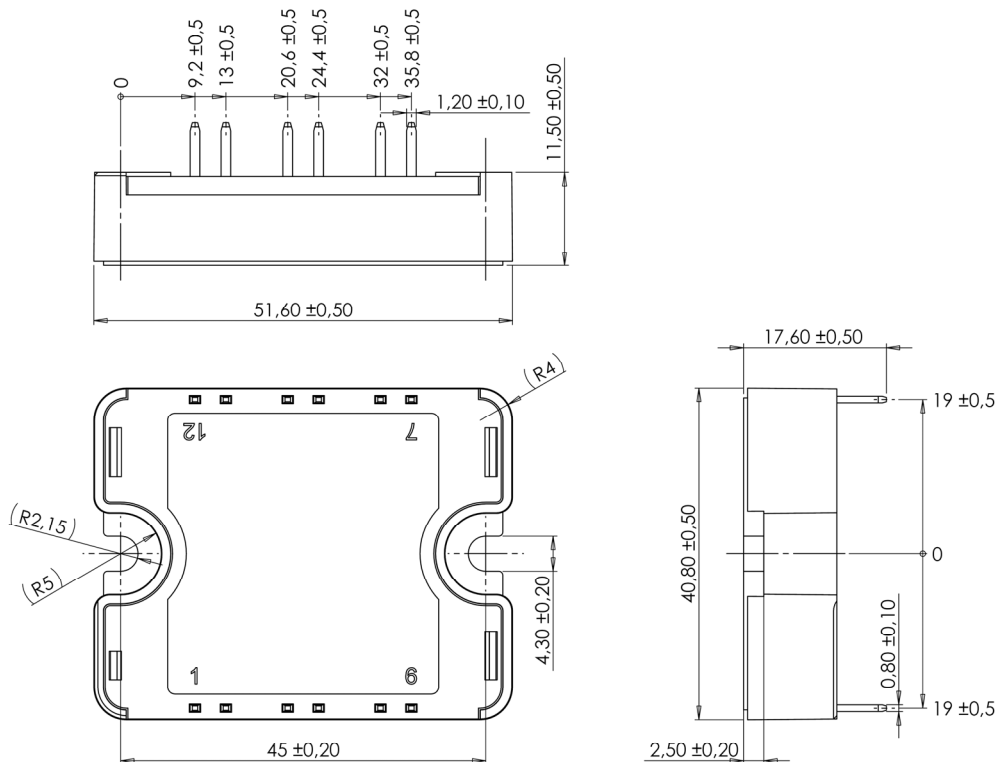
Symbol	Characteristic	Min	Typ	Max	Unit
R ₂₅	Resistance @ 25°C		50		kΩ
ΔR ₂₅ /R ₂₅			5		%
B _{25/85}	T ₂₅ = 298.15 K		3952		K
ΔB/B	T _C = 100°C		4		%

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

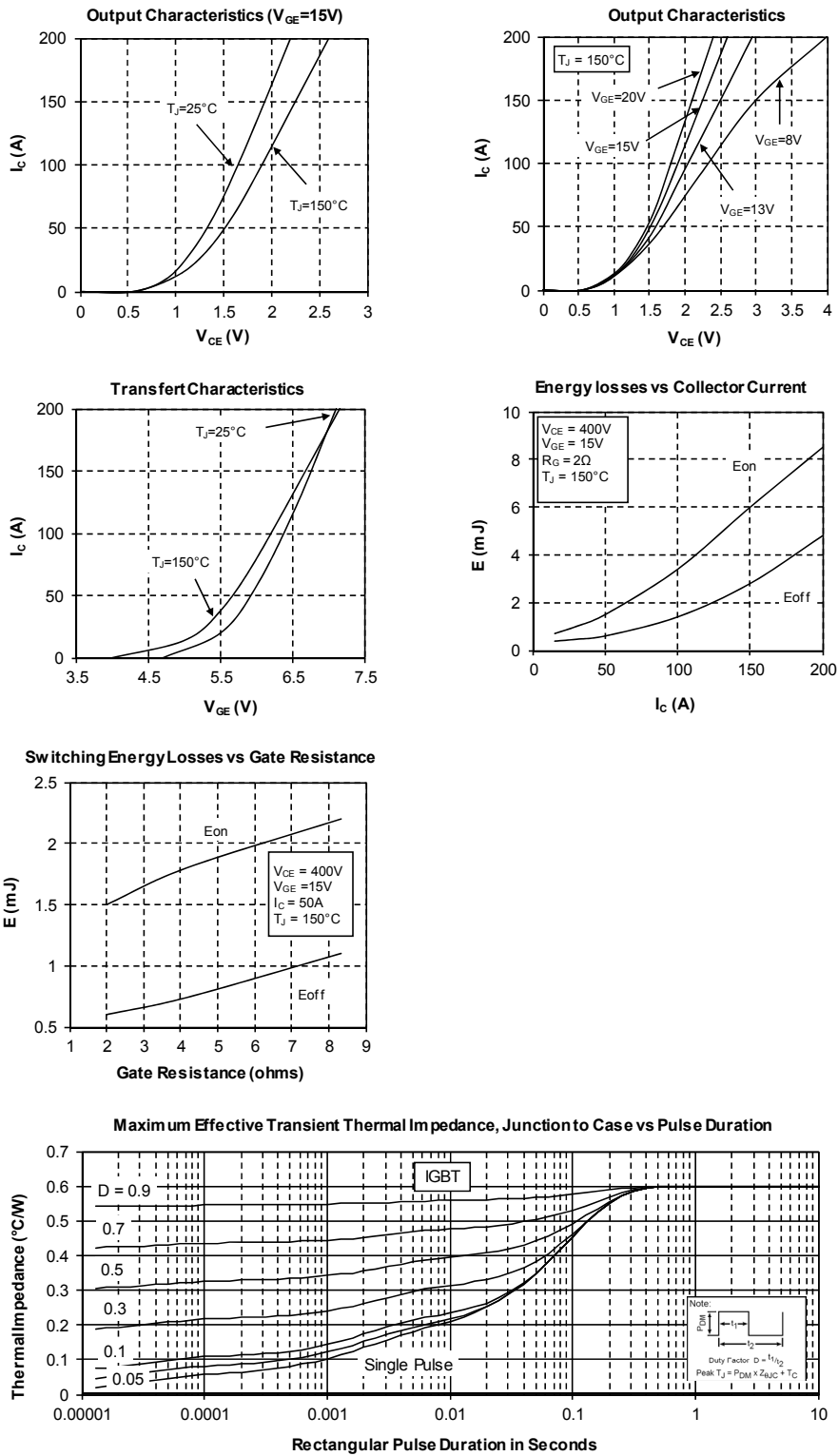
T: Thermistor temperature
 R_T: Thermistor value at T

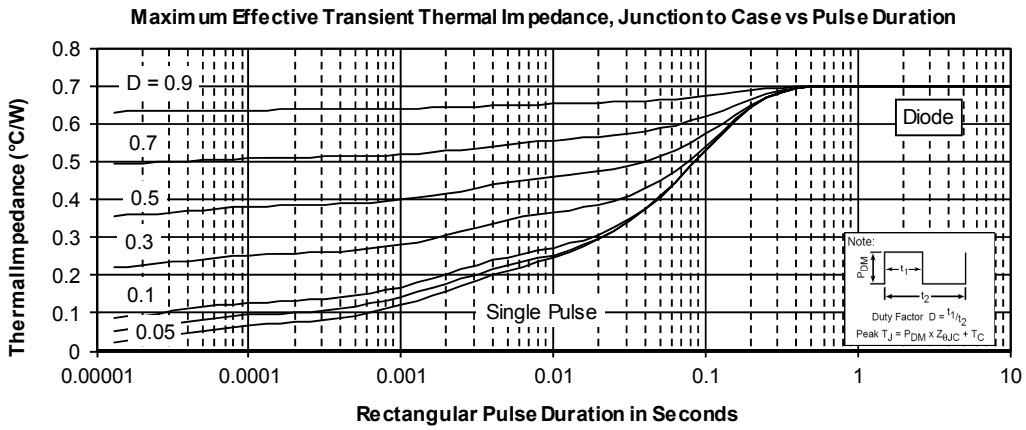
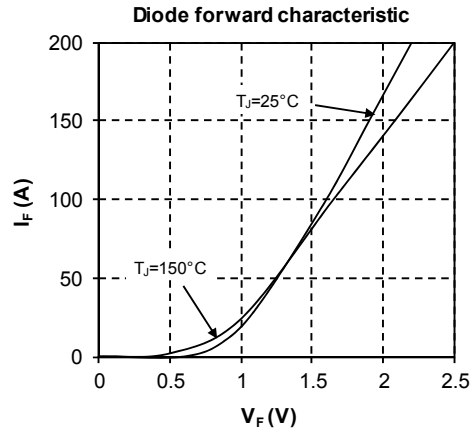
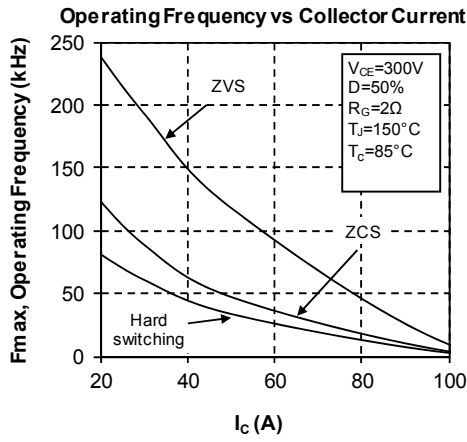
Thermal and package characteristics

Symbol	Characteristic	Min	Max	Unit		
V _{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz	4000		V		
T _J	Operating junction temperature range	-40	175	°C		
T _{JOP}	Recommended junction temperature under switching conditions	-40	T _{Jmax} -25			
T _{STG}	Storage Temperature Range	-40	125			
T _C	Operating Case Temperature	-40	125			
Torque	Mounting torque	To heatsink	M4	2	3	N.m
Wt	Package Weight				80	g

Package outline (dimensions in mm)


See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

Typical performance curve




DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at <http://www.microsemi.com/legal/tnc.asp>

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or indirectly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.