

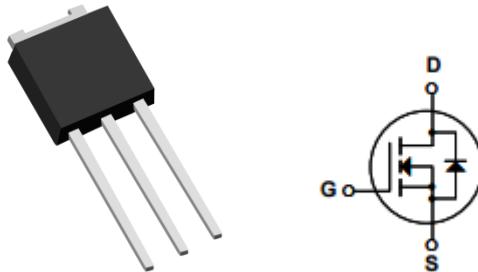
Description

This N-channel MOSFET s use advanced trench technology and design to provide excellent RDS(on) with low gate charge. It can be used in a wide variety of applications.

Features

BVDSS	RDS(on)	ID
60V	0.02Ω	50A

- 1) Low gate charge.
- 2) Green device available.
- 3) Advanced high cell density trench technology for ultra RDS(ON)
- 4) Excellent package for good heat dissipation.



TO-251

Absolute Maximum Ratings $T_c=25^\circ\text{C}$,unless otherwise noted

Symbol	Parameter	Ratings	Units
VDS	Drain-Source Voltage	60	V
VGS	Gate-Source Voltage	±20	V
ID	Continuous Drain Current-1	50	A
	Continuous Drain Current-T=100°C	10.6	
	Pulsed Drain Current2	67.2	
EAS	Single Pulse Avalanche Energy3	155	mJ
PD	Power Dissipation4	38	W
TJ, TSTG	Operating and Storage Junction Temperature Range	-55 to +150	°C

Thermal Characteristics

KERSMI ELECTRONIC CO.,LTD.
60V N-channel MOSFET

Symbol	Parameter	Ratings	Units
R_{JC}	Thermal Resistance ,Junction to Case1	3.28	°C/W
R_{JA}	Thermal Resistance, Junction to Ambient1	110	

Package Marking and Ordering Information

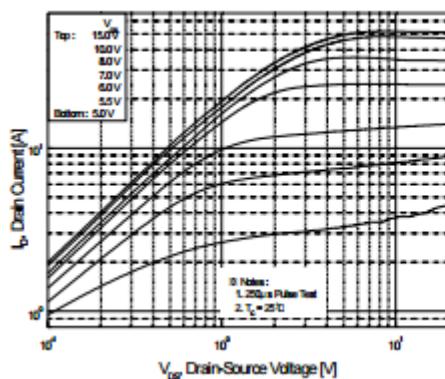
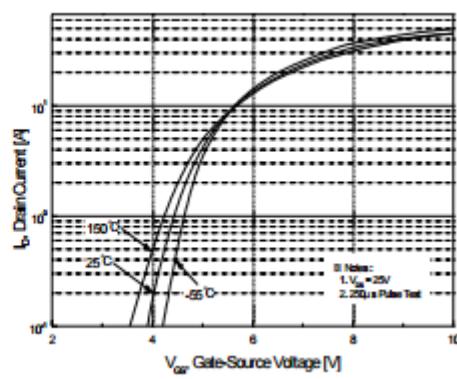
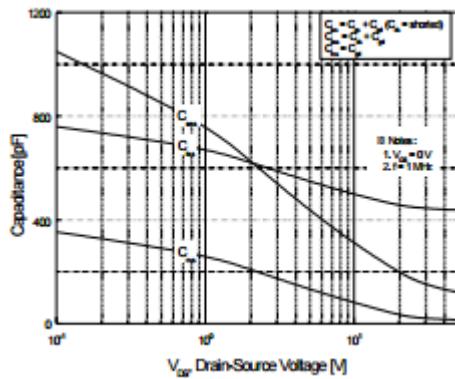
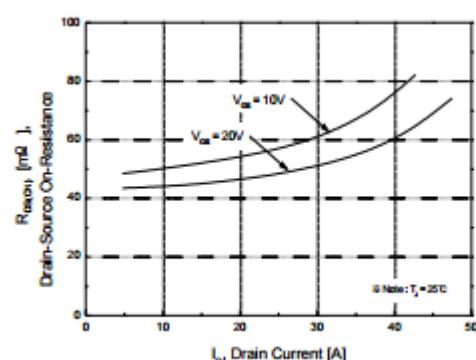
Part NO.	Marking	Package
KSMU50N06	KSMU50N06	TO-251

Electrical Characteristics $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{DS}=0\text{V}, I_D=250\mu\text{A}$	60	—	—	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=0\text{V}, V_{GS}=32\text{V}$	—	—	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{DS}=\pm 20\text{V}, V_{GS}=0\text{A}$	—	—	± 100	nA
On Characteristics						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{DS}=V_{DS}, I_D=250\mu\text{A}$	2.0	—	4.0	V
$R_{DS(on)}$	Drain-Source On Resistance ²	$V_{DS}=10\text{V}, I_D=6\text{A}$	—	0.050	0.02	Ω
		$V_{DS}=2.5\text{V}, I_D=5\text{A}$	—	—	—	---
G_F	Forward Transconductance	$V_{DS}=5\text{V}, I_D=12\text{A}$	—	10	—	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=15\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	—	450	590	pF
C_{oss}	Output Capacitance		—	170	220	
C_{rss}	Reverse Transfer Capacitance		—	25	35	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DS}=20\text{V}, V_{GS}=10\text{V}, R_{GEN}=3.3\Omega$	—	5	20	ns
t_r	Rise Time		—	45	100	ns
$t_{d(off)}$	Turn-Off Delay Time		—	20	50	ns
t_f	Fall Time		—	25	60	ns
Q_g	Total Gate Charge	$V_{GS}=4.5\text{V}, V_{DS}=20\text{V}, I_D=6\text{A}$	—	11.5	15	nC
Q_{gs}	Gate-Source Charge		—	3	—	nC
Q_{gd}	Gate-Drain "Miller" Charge		—	4.5	—	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage ²	$V_{GS}=0\text{V}, I_S=1\text{A}$	—	—	1.5	V
t_{rr}	Reverse Recovery Time	$I_F=7\text{A}, di/dt=100\text{A}/\mu\text{s}$	—	43	—	ns
Q_{rr}	Reverse Recovery Charge		—	50	—	nC

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board 2OZ copper.
2. The data tested by pulse width≤300us,duty cycle≤2%
3. The EAS data shows Max.rating.The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1mH, i_{AS}=17.8A$
4. The power dissipation is limited by 150°C junction temperature.

Typical Characteristics $T_J=25^\circ C$ unless otherwise noted

Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics

Figure 3. Capacitance Characteristics

Figure 4. On-Resistance Variation vs. Drain Current and Gate Voltage

KERSMI ELECTRONIC CO.,LTD.

60V N-channel MOSFET

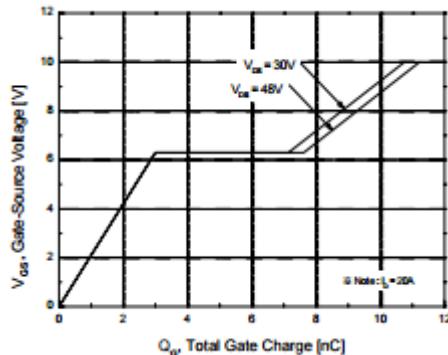


Figure 5. Gate Charge Characteristics

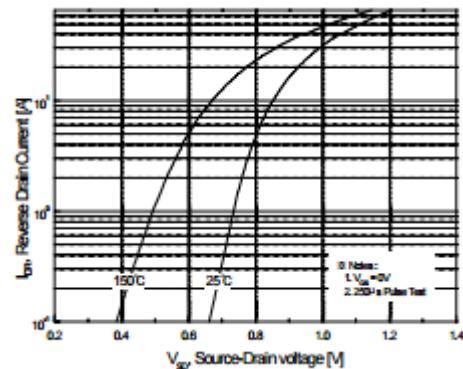


Figure 6. Body Diode Forward Voltage Variation vs. Source Current and Temperature

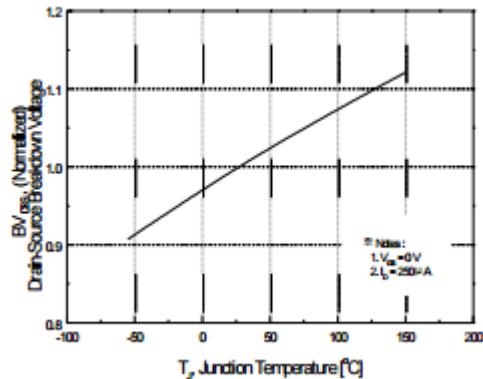


Figure 7.Breakdown Voltage Variation vs. Temperature

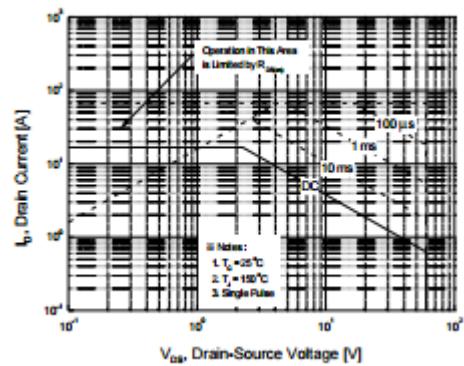


Figure 8.Maximum Safe Operating Area

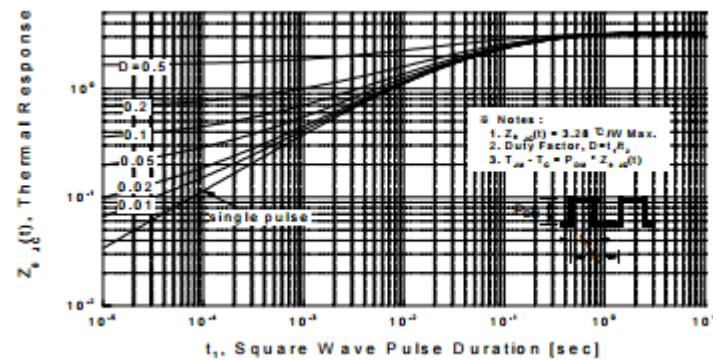


Figure 9. Transient Thermal Response Curve