

N & P-Channel 40-V (D-S) MOSFET

Features

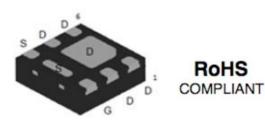
- Low rDS(on) trench technology
- · Low thermal impedance
- · Fast switching speed
- RoHS compliant package

Typical Applications:

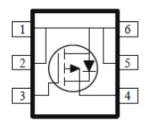
- Load Switches
- Motor Drives
- DC/DC Conversion

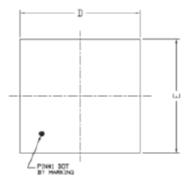
Packing & Order Information

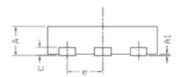
3,000/Reel

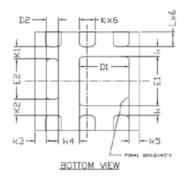


Graphic symbol









Symbol	Dimensions in Millimeters			Dimensions in Inches			
Symbol	MIN	NOM	MAX	MIN	NOM	MAX	
А	0.50	0.55	0.60	0.02	0.022	0.024	
A1	0.00	-	0.05	0.00	-	0.002	
b	0.25	0.30	0.35	0.01	0.012	0.014	
E		0.152 BEF	-		0.006 BEF	-	
D	1.90	2.00	2.10	0.750	0.079	0.083	
D1	0.85	0.95	1.05	0.033	0.037	0.041	
D2	0.13	0.23	0.33	0.005	0.009	0.013	
E	1.90	2.00	2.10	0.075	0.079	0.083	
E1	0.90	1.00	1.10	0.035	0.039	0.043	
E2	0.72	0.82	0.92	0.028	0.032	0.036	
е	0.65 BSC			0.026 BSC			
к	0.20 BSC			0.008 BSC			
K1	0.25 BSC			0.010 BSC			
K2	0.33 BSC			0.013 BSC			
K3	0.22 BSC			0.009 BSC			
K4	0.40 BSC			0.016 BSC			
K5	0.20 BSC			0.008 BSC			
L	0.25	0.30	0.35	0.010	0.012	0.014	



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Absolute Maximum Ratings (T _A =25°C unless otherwise specified)						
Symbol	Parameter	Value	Unit			
V _{DS}	Drain-Source Voltage	-20	V			
V _{GS}	Gate-Source Voltage	±8	V			
I _D	Continuous Drain Current ^a (T _A =25°C)	-8.8	А			
	Continuous Drain Current ^a (T _A =70°C)	-7	А			
I _{DM}	Pulsed Drain Current ^b	-40	А			
I _S	Continuous Source Current (Diode Conduction) ^a	-5	А			
Pn	Power Dissipation ^a (T _A =25°C)	3	W			
	Power Dissipation ^a (T _A =70°C)	1.9	W			
T _J /T _{STG}	Operating Junction and Storage Temperature	-55 to 150	°C			

Thermal Resistance Ratings						
Symbol	Parameter	Maximum	Units			
$R_{ extsf{ heta}JA}$	Maximum Junction-to-Ambient ^a (t <= 10 sec)	40	°C/W			
	Maximum Junction-to-Ambient ^a (Steady-State)	90	0,11			

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

Static						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
V _{GS(th)}	Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4			V
I _{GSS}	Gate-Body Leakage	$V_{DS} = 0 V$, $V_{GS} = \pm 8 V$			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V} , V_{GS} = 0 \text{ V} $ $V_{DS} = -16 \text{ V} , V_{GS} = 0 \text{ V} , T_J = 55^{\circ}\text{C}$			-1 -25	uA
I _{D(on)}	On-State Drain Current	$V_{DS} = -5 V, V_{GS} = -4.5 V$	-12			А
r DS(on)	Drain-Source On-Resistance	$V_{GS} = -4.5 \text{ V}, I_D = -7 \text{ A}$ $V_{GS} = -2.5 \text{ V}, I_D = -5.6 \text{ A}$			26 34	mΩ
g _{fs}	Forward Tranconductance	V _{GS} = -15 V, I _D = -7 A		8		S
V _{SD}	Diode Forward Voltage	$I_{\rm S}$ = -2.5 A , $V_{\rm GS}$ = 0 V		-0.68		V



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Dynamic ^b						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
Qg	Total Gate Charge	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -7 \text{ A},$ - $V_{GS} = -4.5 \text{ V}$		30		nC
Q _{gs}	Gate-Source Charge			4		nC
Q_{gd}	Gate-Drain Charge			6		nC
t _{d(on)}	Turn-On Delay Time	$I_{D} = -7 \text{ A}, R_{L} = 1.4 \Omega,$ $V_{GEN} = -4.5 \text{ V}, R_{GEN} = 6 \Omega$ $V_{DS} = -10 \text{ V}$		6		ns
t _r	Rise Time			12		ns
t _{d(off)}	Turn-Off Delay Time			85		ns
tf	Fall Time			35		ns
C _{ISS}	Input Capacitance	V _{DS} = -15 V f = 1 MHz ,V _{GS} = 0 V		1435		pF
C _{OSS}	Output Capacitance			126		pF
C _{RSS}	Reverse Transfer Capacitance			113		pF

Notes

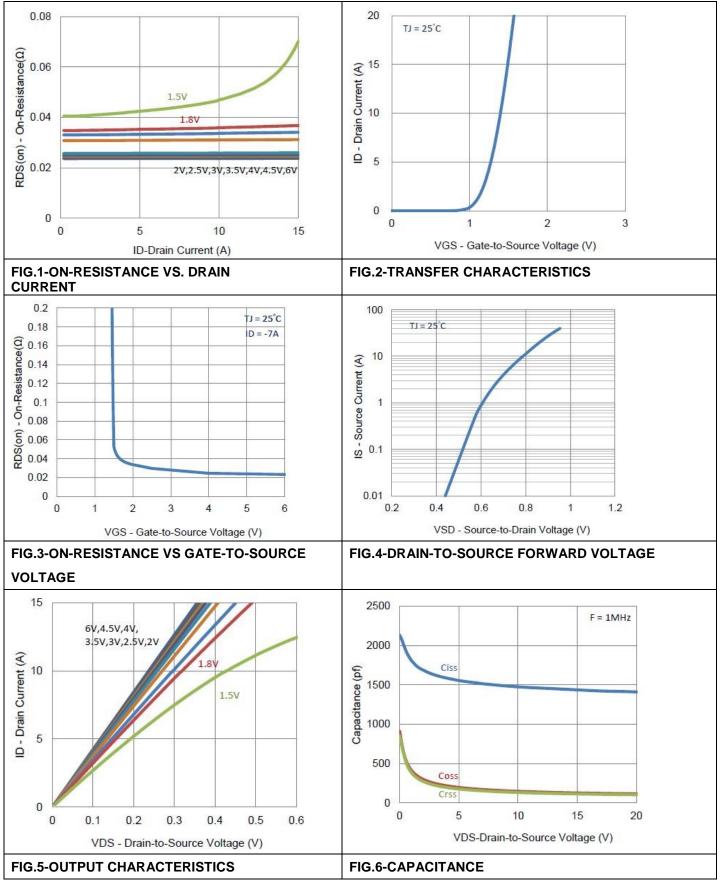
a. Pulse test: PW <= 300us duty cycle <= 2%.

b. Guaranteed by design, not subject to production testing.



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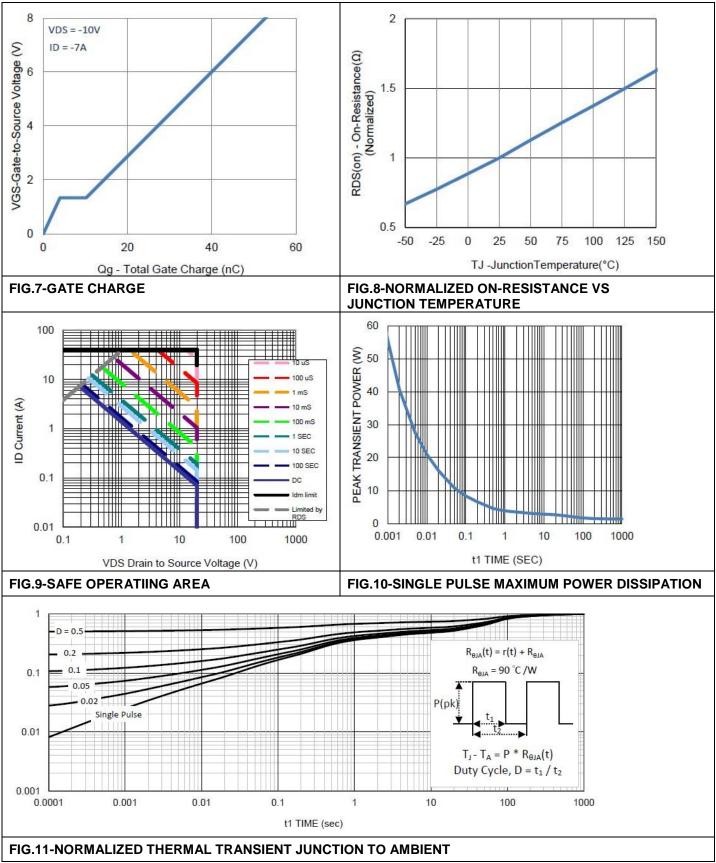
Typical Electrical Characteristics





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