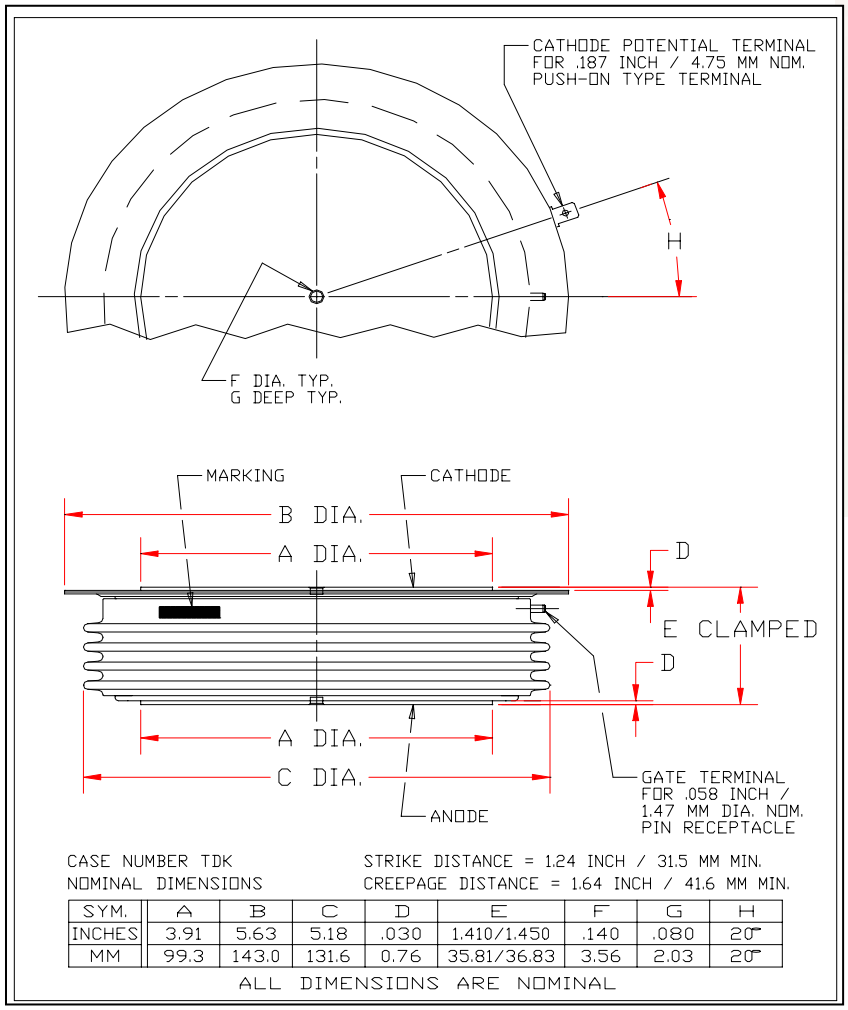
TDK4_ _3302

Phase Control Thyristor

3300 Amperes 4500 Volts

Powerex, Inc., 200 Hillis Street, Youngwood, Pennsylvania 15697-1800 (724)925-7272

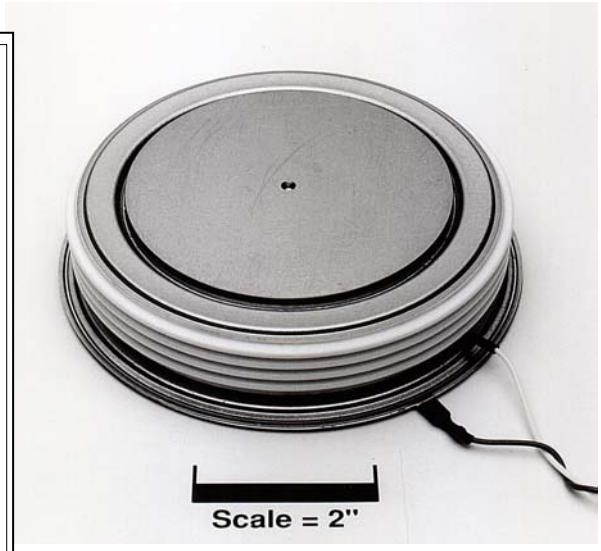


CASE NUMBER TDK
NOMINAL DIMENSIONS

STRIKE DISTANCE = 1.24 INCH / 31.5 MM MIN.
CREEPAGE DISTANCE = 1.64 INCH / 41.6 MM MIN.

SYM.	A	B	C	D	E	F	G	H
INCHES	3.91	5.63	5.18	.030	1.410/1.450	.140	.080	20°
MM	99.3	143.0	131.6	0.76	35.81/36.83	3.56	2.03	20°

ALL DIMENSIONS ARE NOMINAL



The TDK4 is a high voltage, high current disc pack SCR employing a high di/dt gate structure. This gate design allows the SCR to be reliably operated at high di/dt and dv/dt conditions in various phase control applications.

FEATURES:

- Low On-State Voltage
- High di/dt Capability
- High dv/dt Capability
- Hermetic Ceramic Package
- Excellent Surge and I^2t Ratings

APPLICATIONS:

- DC Power Supplies
- Motor Controls

ORDERING INFORMATION

Select the complete 12 digit Part Number using the table below.
EXAMPLE: TDK4443302DH is a 4400V-3300A SCR with 300ma IGT and 12 inch gate and cathode potential leads.

PART	Voltage Rating	Voltage Code	Current Rating	Current Code	Turn-Off	Gate	Leads
	V_{DRM}-V_{RRM}		I_{tavg}		Tq	I_{GT}	
TDK4	4500	**45**	3300	**33**	**0**	**2**	
	4400	**44**					
	4200	**42**			400us	300ma	12"
	4000	**40**			(typ.)	(max)	
	3600	**36**					

Revised: 7/27/2003

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Absolute Maximum Ratings†

Characteristic	Symbol	Rating	Units
Repetitive Peak Voltage	V_{DRM}-V_{RRM}	4400	Volts
Average On-State Current, T_C=72°C	$I_{T(Avg.)}$	3300	A
RMS On-State Current, T_C=70°C	$I_{T(RMS)}$	5184	A
Average On-State Current, T_C=55°C	$I_{T(Avg.)}$	3900	A
RMS On-State Current, T_C=55°C	$I_{T(RMS)}$	6126	A
Peak One Cycle Surge Current, 60Hz, V_R=0V	I_{TSM}	50,000	A
Peak One Cycle Surge Current, 50Hz, V_R=0V	I_{TSM}	47,140	A
Fuse Coordination I^2t, 60Hz	I^2t	1.04E+07	A^2s
Fuse Coordination I^2t, 50Hz	I^2t	1.11E+07	A^2s
Critical Rate-of-Rise of On-State Current Repetitive from .67•VDRM	di/dt	150	A/us
Critical Rate-of-Rise of On-State Current Non-Repetitive from .67•VDRM	di/dt	300	A/us
Peak Gate Power, 100us	P_{GM}	16	Watts
Average Gate Power	$P_{G(avg)}$	5	Watts
Operating Temperature	Tj	-40 to+125	°C
Storage Temperature	$T_{Stg.}$	-40 to+150	°C
Approximate Weight		7	lb
		3.18	Kg
Mounting Force		18,000 - 25,000	lbs
		80 - 110	KNewtons

† Ratings apply for operation at rated load force.

Electrical Characteristics, Tj=25°C unless otherwise specified

Characteristic	Symbol	Test Conditions	Rating min	Rating typ	Rating max	Units
Repetitive Peak Forward Leakage Current	I_{DRM}	Tj=125°C, V_{DRM}=Rated			300	ma
Repetitive Peak Reverse Leakage Current	I_{RRM}	Tj=125°C, V_{RRM}=Rated			300	ma
Peak On-State Voltage	V_{TM}	Tj=125°C, I_{TM}=3000A			1.60	V
V_{TM} Model, Low Level	V_0	Tj=125°C			0.991269	V
VTM = V_O + r•I_{TM}	r	15% I_{TM} - π•I_{TM}			1.96E-04	Ω
V_{TM} Model, High Level	V_0	Tj=125°C			0.772002	V
VTM = V_O + r•I_{TM}	r	π•I_{TM} - I_{TSM}			2.18E-04	Ω
V_{TM} Model, 4-Term	A	Tj=125°C			0.132	
V_{TM}= A + B•Ln(I_{TM}) +	B	15%I_{TM} - I_{TSM}			0.181	
C•(I_{TM}) + D•(I_{TM})^{½}	C				2.57E-04	
	D				-1.41E-02	
Turn-On Delay Time	t_d	V_D = 0.5•V_{DRM} Gate Drive: 40V - 20Ω			3	us
Turn-Off Time (typ)	tq	Tj=125°C dv/dt = 20V/us to 80% V_{DRM}			400	us
$dv/dt_{(Crit)}$	dv/dt	Tj=125°C Exp. Waveform V_D =67% Rated	2000			V/us
Gate Trigger Current	I_{GT}	Tj=25°C V_D = 12V	40	100	300	ma
Gate Trigger Voltage	V_{GT}		0.8	2.0	4.0	V
Peak Reverse Gate Voltage	V_{GRM}				5	V

Thermal Characteristics

Characteristic	Symbol	Test Conditions	Rating min	Rating typ	Rating max	Units
Thermal Resistance						
Junction to Case	$R\Theta_{jc}$	Double side cooled		0.0055	0.007	°C/Watt
Case to Sink	$R\Theta_{cs}$	Double side cooled		0.0015	0.002	°C/Watt
Thermal Impedance Model	$Z\Theta_{jc}$	Double side cooled				

$$Z\Theta_{jc}(t) = \Sigma(A(N)\bullet(1-\exp(-t/Tau(N))))$$

where:

N =	1	2	3	4
A(N) =	1.43E-04	9.38E-04	2.42E-03	3.50E-03
Tau(N) =	2.62E-03	2.31E-02	3.05E-01	3.30E+00

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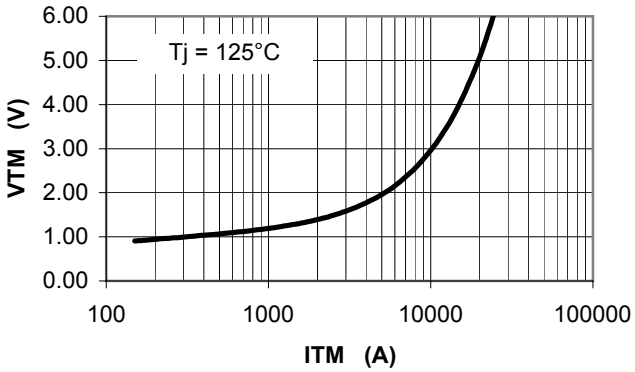
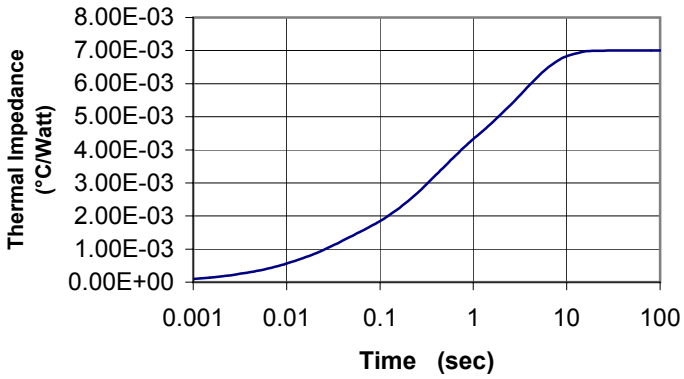
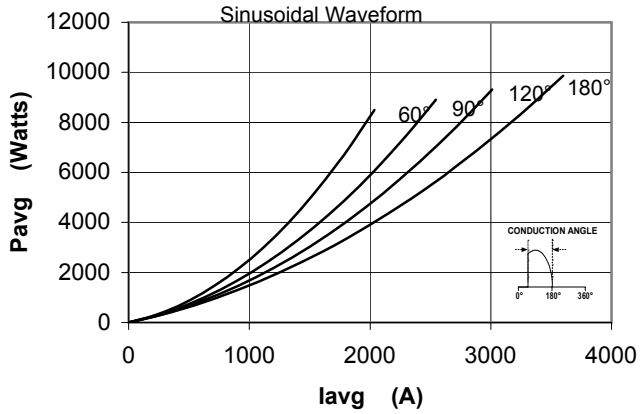
Maximum On-State Voltage Drop



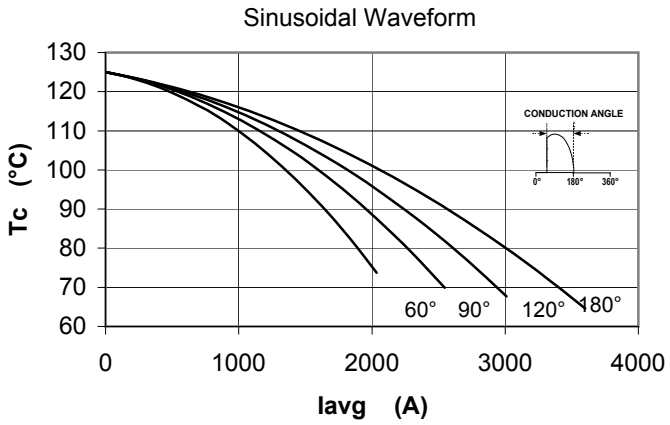
MAXIMUM TRANSIENT THERMAL IMPEDANCE



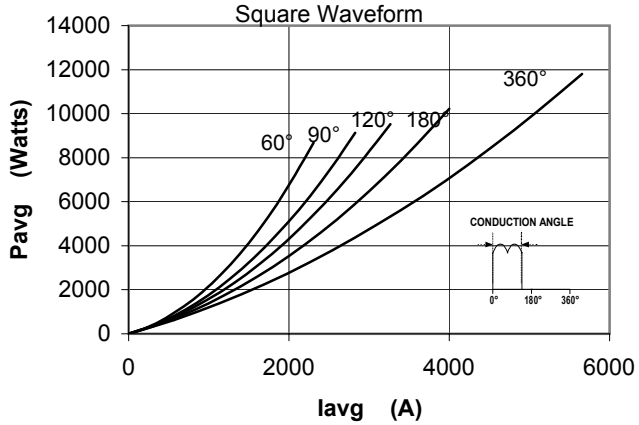
Maximum On-State Power Dissipation



Maximum Allowable Case Temperature



Maximum On-State Power Dissipation



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