

da

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MMBTA42

Features

- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- Epoxy meets UL 94 V-0 flammability rating
- Moisure Sensitivity Level 1
- Capable of 300mWatts of Power Dissipation
- Continuous Collector Current : 300mA
- Marking:1D

Electrical Characteristics @ 25°C Unless Otherwise Specified

Symbol	Parameter	Min	Max	Units	
OFF CHARA	OFF CHARACTERISTICS				
$V_{(BR)CEO}$	$V_{(BR)CEO}$ Collector-Emitter Breakdown Voltage* 300 ($I_C=1.0$ mAdc, $I_B=0$)				
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage (I _C =100μAdc, I _E =0)	ge 300 Vd			
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage (I _E =100μAdc, I _C =0)	6.0		Vdc	
I _{CBO}	I_{CBO} Collector Cutoff Current 0.1 $(V_{CB}=200\text{Vdc}, I_E=0)$		uAdc		
I _{EBO}	EBO Emitter Cutoff Current $(V_{EB}=6.0Vdc, I_C=0)$		0.1	uAdc	

ON CHARACTERISTICS

h _{FE}	DC Current Gain*			
	$ \begin{array}{l} (I_{\text{C}} = 1.0 \text{mAdc}, \ V_{\text{CE}} = 10 \text{Vdc}) \\ (I_{\text{C}} = 10 \text{mAdc}, \ V_{\text{CE}} = 10 \text{Vdc}) \\ (I_{\text{C}} = 30 \text{mAdc}, \ V_{\text{CE}} = 10 \text{Vdc}) \end{array} $	25 40 40		
V _{CE(sat)}	Collector-Emitter Saturation Voltage (I _C =20mAdc, I _B =2.0mAdc)		0.5	Vdc
V _{BE(sat)}	Base-Emitter Saturation Voltage (I _C =20mAdc, I _B =2.0mAdc)		0.9	Vdc

SMALL-SIGNAL CHARACTERISTICS

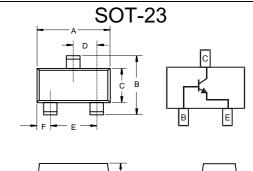
f⊤	Current Gain-Bandwidth Product (I _C =10mAdc, V _{CE} =20Vdc, f=100MHz)	50		MHz
C _{cb}	Collector-Emitter Capacitance (V _{CR} =20Vdec, I _E =0, f=1.0MHz)		3.0	рF

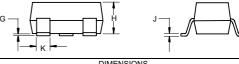
THERMAL CHARACTERISTICS

THERMAL CHARACTERISTICS			
Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board, ⁽¹⁾ $T_{\Delta} = 25^{\circ}C$	P_{D}	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (2) T _A = 25°C	P _D	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T_J , T_{stg}	-55 to +150	°C

Voltage Transistor

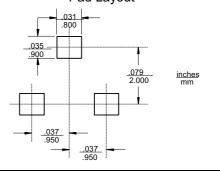
NPN Silicon High





DIMENSIONS					
	INCHES		N	1M	
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.110	.120	2.80	3.04	
В	.083	.098	2.10	2.64	
С	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
E	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
Ι	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	

Suggested Solder Pad Layout



^{*}Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2.0\%$



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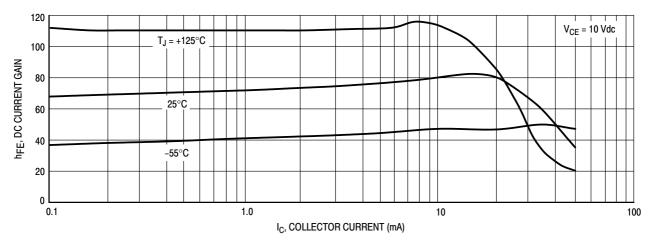


Figure 1. DC Current Gain

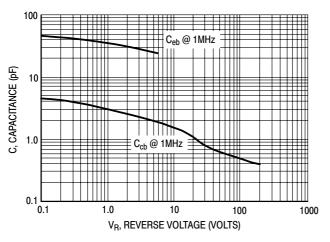


Figure 2. Capacitance

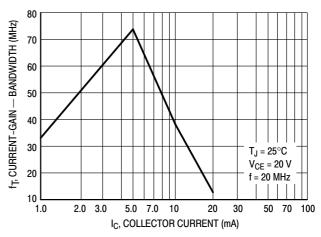
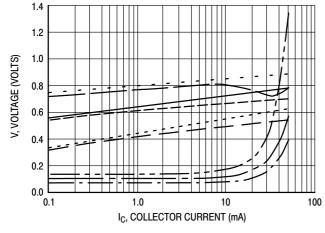
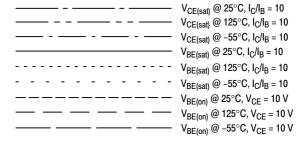


Figure 3. Current-Gain - Bandwidth







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Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel 3Kpcs/Reel

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