UNISONIC TECHNOLOGIES CO., LTD

SFR1020C DIODE

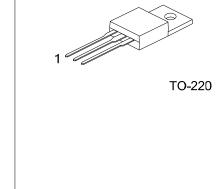
ULTRA-FAST RECOVERY RECTIFIER DIODES

DESCRIPTION

UTC SFR1020C is dual center tap rectifier suited for high frequency Switching Mode PowerSupplies applications.

FEATURES

- * High Surge Current Capability
- * Suited For Smps, DC ~ DC Converters
- * Low Forward And Reverse Recovery Time
- * Low Losses



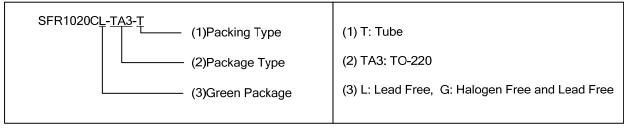
CONNECTION DIAGRAM



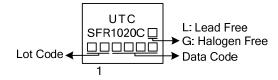
ORDERING INFORMATION

Order Nu	Deelsese	Pin Assignment			Doolsing	
Lead Free	Halogen Free	Package	1	2	3	Packing
SFR1020CL-TA3-T	SFR1020CG-TA3-T	TO-220	A1	K	A2	Tube

Note: Pin Assignment: A: Anode K: Cathode



MARKING



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■ ABSOLUTE MAXIMUM RATING (limiting values, per leg)

PARAMETER	SYMBOL	RATINGS	UNIT
Repetitive Peak Reverse Voltage	V_{RRM}	200	V
RMS Forward Current	I _{F(RMS)}	10	Α
Average Forward Current δ = 0.5 T _C =125°C (Per leg)	I _{F(AV)}	5	Α
Surge Non Repetitive Forward Current, t _p =10ms Sinusoidal	I _{FSM}	50	Α
Storage temperature range	T _{stg}	-60 ~ +150	°C

Note 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (per leg)

PARAMETER	SYMBOL	TEST COND	ITIONS	MIN	TYP	MAX	UNIT
Reverse Leakage Current		T _J = 25°C	\/ - \/			50	μA
(Note1)	IR	T _J = 100°C	$V_R = V_{RRM}$			0.6	mA
Forward Voltage Drop	V	T _J = 25°C	I _F = 5 A			0.9	V
(Note2)	V_F	T _J = 125°C	I _F = 5 A		0.69	0.74	V

Note1. tp = 5 ms, δ < 2 %

2. tp = 380 μ s, δ < 2 %

To evaluate the conduction losses use the following equation: P = $0.78 \times I_{F(AV)} + 0.042 \times IF^{2}(RMS)$

■ RECOVERY CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C$, $I_F = 0.5A$, $V_F = 30V$, $I_R = 1A$			40	ns
Formard Recovery Time	t _{fr}	$T_J = 25^{\circ}C$, $I_F = 1A$, $dI_F/d_t = 50 A/\mu s$ $V_R = 30V$		33		ns
	V_{FP}	$T_J = 25^{\circ}C$, $I_F = 1A$, $dI_F/d_t = 50 A/\mu s$		3		V

Note: When diodes 1 and 2 are used simultaneously:

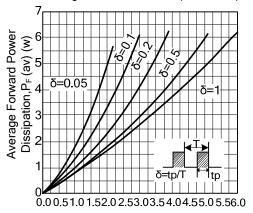
 ΔT_J (diode 1) = P(diode 1)× $R_{th(j-c)}$ (per leg) + P(diode 2) × $R_{th(c)}$

^{2.} The device is guaranteed to meet performance specification within 0°C~70°C operating temperature range and assured by design from –20°C~85°C.

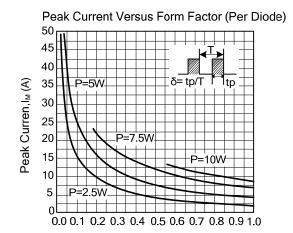
SFR1020C DIODE

■ TYPICAL CHARACTERISTICS

Average Forward Power Dissipation Versus Average Forward Current (Per Diode)

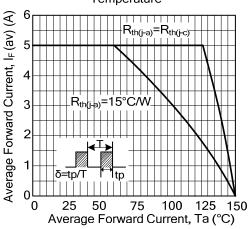


Average Forward Current, IF (av) (A)

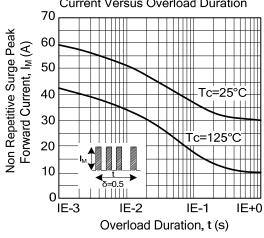


Form Factor,δ

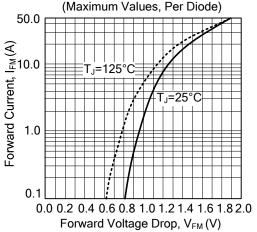
Average Forward Current Versus Ambient Temperature



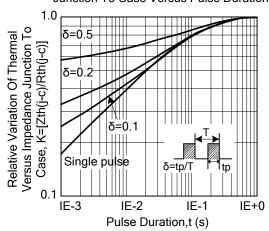
Non Repetitive Surge Peak Forward Current Versus Overload Duration



Forward Voltage Drop Versus Forward Current (Maximum Values, Per Diode)



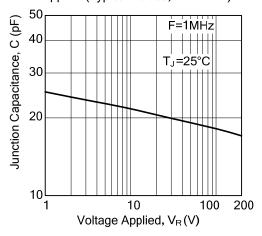
Relative Variation Of Thermal Versus Impedance Junction To Case Versus Pulse Duration



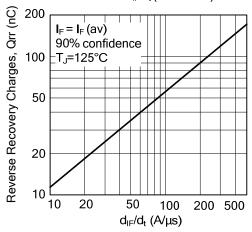
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■ TYPICAL CHARACTERISTICS (Cont.)

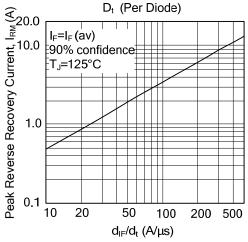
Junction Capacitance Versus Reverse Voltage Applied (Typical Values, Per Diode)



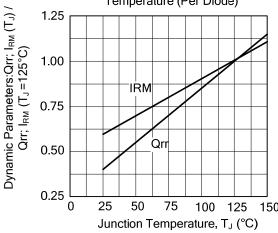
Reverse Recovery Charges Versus D_{if}/D_t (Per Diode)



Peak Reverse Recovery Current Versus Dif



Dynamic Parameters Versus Junction Temperature (Per Diode)



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