## **2SJ588**

# Silicon P Channel MOS FET High Speed Switching

## **HITACHI**

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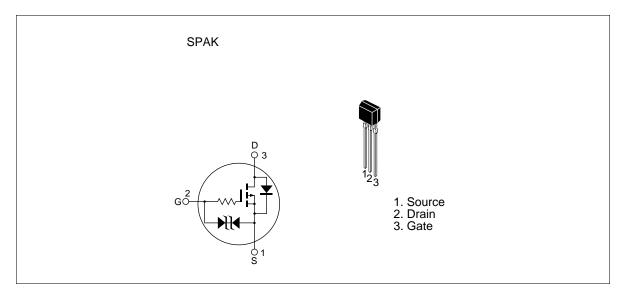
### **Features**

• Low on-resistance

$$R_{DS}$$
 =2.8  $\Omega$  typ. (V  $_{GS}$  = -10 V ,  $I_D$  = -50 mA) 
$$R_{DS}$$
 =5.7  $\Omega$  typ. (V  $_{GS}$  = -4 V ,  $I_D$  = -50 mA)

• 4 V gate drive device.

### **Outline**





## 2SJ588

## **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub>	-30	V	
Gate to source voltage	V <sub>GSS</sub>	±20	V	
Drain current	I <sub>D</sub>	-100	mA	
Drain peak current	Note1 D(pulse)	-400	mA	
Body-drain diode reverse drain current	I <sub>DR</sub>	-100	mA	
Channel dissipation	Pch	300	mW	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Note: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

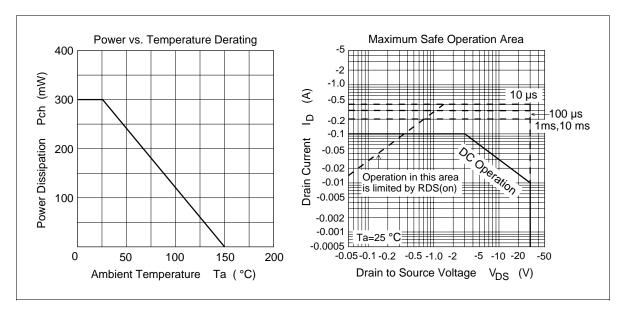
## Electrical Characteristics ( $Ta = 25^{\circ}C$ )

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-30	_	_	V	$I_D = -100  \mu A,  V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±5	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	_	_	-1	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.3	_	-2.3	V	$I_D = -10\mu A, V_{DS} = -5 V$
Static drain to source on state	$R_{\text{DS(on)}}$	_	2.8	3.3	Ω	$I_D = -50 \text{ mA}, V_{GS} = -10 \text{ V}^{\text{Note 2}}$
resistance	$R_{\text{DS(on)}}$	_	5.7	7.9	Ω	$I_D = -50 \text{ mA}, V_{GS} = -4 \text{ V}^{\text{Note 2}}$
Forward transfer admittance	y <sub>fs</sub>	68	105	_	mS	$I_D = -50 \text{ mA}, V_{DS} = -10 \text{ V}^{\text{Note 2}}$
Input capacitance	Ciss	_	25	_	pF	V <sub>DS</sub> = -10 V
Output capacitance	Coss	_	20	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	8	_	pF	f = 1 MHz
Turn-on delay time	$\mathbf{t}_{\text{d(on)}}$	_	10	_	ns	$I_{\rm D}$ = -50 mA, $V_{\rm GS}$ = -10 V
Rise time	t <sub>r</sub>		15		ns	$R_L = 200\Omega$
Turn-off delay time	$t_{\text{d(off)}}$	_	40	_	ns	
Fall time	$t_{f}$	_	45	_	ns	

Note: 2. Pulse test

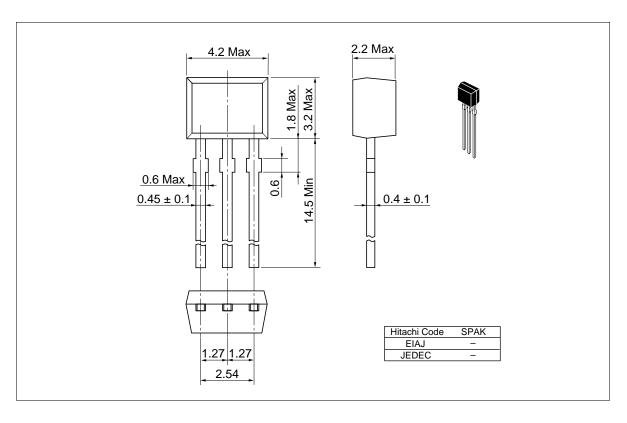
See characteristics curves of 2SJ575

### **Main Characteristics**



## **Package Dimensions**

Unit: mm



#### **Cautions**

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