

650V, 45A, $V_{CE(on)}$ = 1.9V Typical

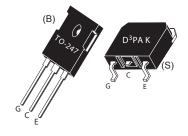
Ultra Fast NPT - IGBT®

The Ultra Fast 650V NPT-IGBT® family of products is the newest generation of IGBTs optimized for outstanding ruggedness and best trade-off between conduction and switching losses.

Features

- · Low Saturation Voltage
- Low Tail Current
- RoHS Compliant

- Short Circuit Withstand Rated
- High Frequency Switching
- Ultra Low Leakage Current





Unless stated otherwise, Microsemi discrete IGBTs contain a single IGBT die. This device is recommended for applications such as induction heating (IH), motor control, general purpose inverters and uninterruptible power supplies (UPS).

MAXIMUM RATINGS

All Ratings: $T_C = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameter	Ratings	Unit
V _{ces}	Collector Emitter Voltage	650	V
$V_{\rm GE}$	Gate-Emitter Voltage	±30	V
I _{C1}	Continuous Collector Current @ T _c = 25°C	118	
I _{C2}	Continuous Collector Current @ T _C = 110°C	56	Α
I _{CM}	Pulsed Collector Current ①	224	
SCWT	Short Circuit Withstand Time: $V_{CE} = 325V$, $V_{GE} = 15V$, $T_{C} = 125^{\circ}C$	10	μs
$P_{\scriptscriptstyle D}$	Total Power Dissipation @ T _c = 25°C	543	W
T _J ,T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C
T _L	Max. Lead Temp. for Soldering: 0.063" from Case for 10 Sec.	300	C

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Min	Тур	Max	Unit
V _{(BR)CES}	Collector-Emitter Breakdown Voltage (V _{GE} = 0V, I _C = 250uA)	650			
V _{GE(TH)}	Gate Threshold Voltage $(V_{CE} = V_{GE}, I_{C} = 1.0 \text{mA}, T_{j} = 25 ^{\circ}\text{C})$	3.5	5.0	6.5	
V _{CE(ON)}	Collector-Emitter On Voltage (V _{GE} = 15V, I _C = 45A, T _j = 25°C)		1.9	2.4	Volts
	Collector-Emitter On Voltage (V _{GE} = 15V, I _C = 45A, T _j = 125°C)		2.4		
	Collector-Emitter On Voltage ($V_{GE} = 15V$, $I_{C} = 90A$, $T_{j} = 25^{\circ}C$)		2.6		
I _{CES}	Collector Cut-off Current (V _{CE} = 650V, V _{GE} = 0V, T _j = 25°C) ②		10	250	μΑ
	Collector Cut-off Current (V _{CE} = 650V, V _{GE} = 0V, T _j = 125°C) ②		100		
I _{GES}	Gate-Emitter Leakage Current (V _{GE} = ±20V)			±250	nA

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C _{ies}	Input Capacitance	Capacitance		2900		
C _{oes}	Output Capacitance	$V_{GE} = 0V, V_{CE} = 25V$		548		pF
C _{res}	Reverse Transfer Capacitance	f = 1MHz		268		
V_{GEP}	Gate to Emitter Plateau Voltage	Gate Charge		7.5		V
Q ³	Total Gate Charge	V _{GE} = 15V		150	203	
Q _{ge}	Gate-Emitter Charge	V _{CE} = 325V		18	24	nC
Q_{gc}	Gate- Collector Charge	I _C = 45A		74	100	
t _{d(on)}	Turn-On Delay Time	Inductive Switching (25°C)		15		
t,	Current Rise Time	V _{cc} = 433V		32		20
t _{d(off)}	Turn-Off Delay Time	V _{GE} = 15V		100		ns
t _f	Current Fall Time	I _C = 45A		50		
E _{on2} ⑤	Turn-On Switching Energy	$R_{G} = 4.3\Omega^{4}$		900	1350	1
E _{off}	Turn-Off Switching Energy	T _J = +25°C		580	870	μJ
t _{d(on)}	Turn-On Delay Time	Inductive Switching (125°C)		15		
t,	Current Rise Time	V _{cc} = 433V		32		20
t _{d(off)}	Turn-Off Delay Time	V _{GE} = 15V		123		ns
t _f	Current Fall Time	I _C = 45A		52		
E _{on2} 5	Turn-On Switching Energy	$R_{_{\rm G}} = 4.3\Omega^{\oplus}$		925	1245	1
E _{off}	Turn-Off Switching Energy	T _J = +125°C		800	1160	μJ

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic	Min	Тур	Max	Unit
R _{eJC}	Junction to Case Thermal Resistance			0.23	°C/W
$R_{_{\theta JA}}$	Junction to Ambient Thermal Resistance			40	
W _T	Package Weight		0.22		oz
			6.2		g
Torque	Mounting Torque (TO-247 Package), 4-40 or M3 screw			10	in-lbf
				6.2	N·m

- 1 Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.
- 2 Pulse test: Pulse Width < $380\mu s$, duty cycle < 2%.
- 3 See Mil-Std-750 Method 3471.
- 4 $R_{\rm g}$ is external gate resistance, not including internal gate resistance or gate driver impedance. (MIC4452)
- 5 E_{on2} is the energy loss at turn-on and includes the charge stored in the freewheeling diode.
- 6 $\,$ E $_{
 m off}$ is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1.

Microsemi reserves the right to change, without notice, the specifications and information contained herein.

TYPICAL PERFORMANCE CURVES

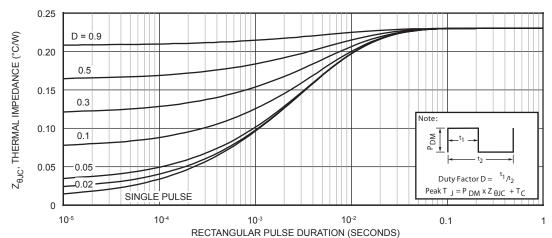
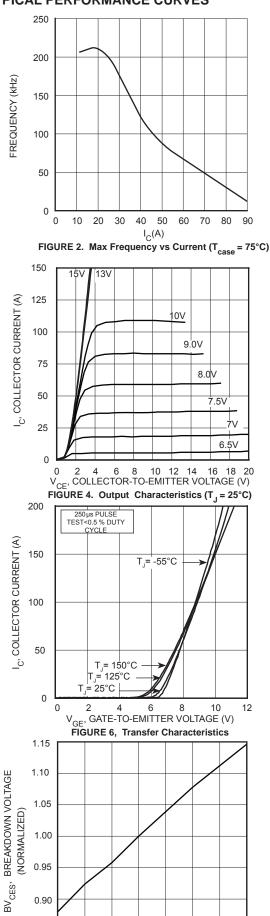
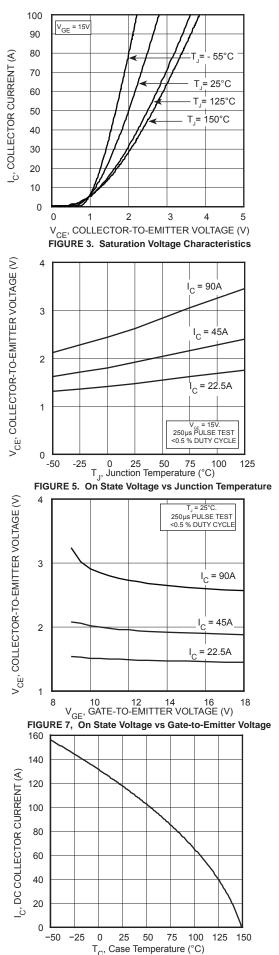


FIGURE 1. Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration





Rev

-25

0 25 50

T_J, JUNCTION TEMPERATURE

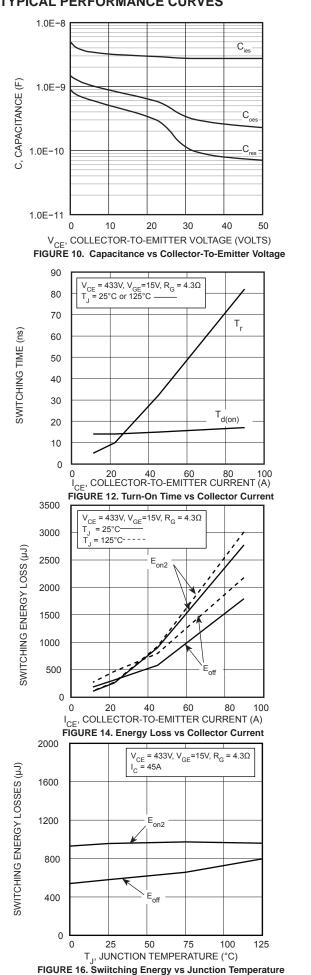
FIGURE 8. Breakdown Voltage vs Junction Temperature

75

100 125

0.90

0.85



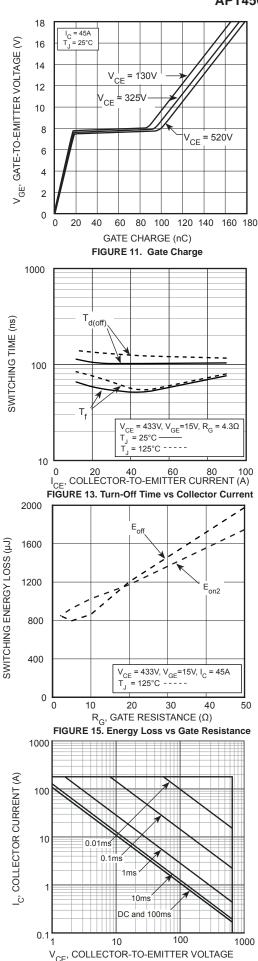


FIGURE 17. Minimum Switching Safe Operating Area

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В

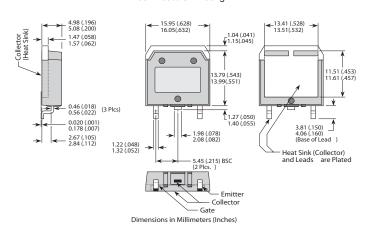
Rev

052-6414

TO-247 Package Outline

4.69 (.185) 5.31 (.209) 1.49 (.059) 2.49 (.098) 5.38 (.212) 6.20 (.244) 6.15 (.242) BSC 20.80 (.819) 21.46 (.845) \bigcirc Collector 3.50 (.138) 3.81 (.150) 4.50 (.177) Max. 0.40 (.016) 0.79 (.031) 1.01 (.040) 1.40 (.055) Collector Emitter 5.45 (.215) BSC 2-Plcs. Dimensions in Millimeters and (Inches)

D³PAK Package Outline e3 : 100% Sn Plating



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