Multilayer Ceramic Chip Capacitors

C2012X6S0J106M085AC







1 of 3 Creation Date : May 17, 2017 (GMT)



TDK item description C2012X6S0J106MT****

Applications	Commercial Grade	
Feature	General General (Up to 50V)	
Series	C2012 [EIA 0805]	
Status	Production (Not Recommended for New Design)	



Size		
Length(L)	2.00mm ±0.20mm	
Width(W)	1.25mm ±0.20mm	
Thickness(T)	0.85mm ±0.15mm	
Terminal Width(B)	0.20mm Min.	
Terminal Spacing(G)	0.50mm Min.	
Recommended Land Pattern (PA)	1.00mm to 1.30mm(Flow Soldering)	
neconimended Land Fattern (FA)	0.90mm to 1.20mm(Reflow Soldering)	
Recommended Land Pattern (PB)	1.00mm to 1.20mm(Flow Soldering)	
Neconimented Land Fattern (FB)	0.70mm to 0.90mm(Reflow Soldering)	
Recommended Land Pattern (PC)	0.80mm to 1.10mm(Flow Soldering)	
neconfinenced Land Facterif (FC)	0.90mm to 1.20mm(Reflow Soldering)	

Electrical Characteristics		
Capacitance	10μF ±20%	
Rated Voltage	6.3VDC	
Temperature Characteristic	X6S(±22%)	
Dissipation Factor (Max.)	10%	
Insulation Resistance (Min.)	10ΜΩ	

O ther	
Coldaring Mathed	Wave (Flow)
Soldering Method	Reflow
AEC-Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000pcs

[!] Images are for reference only and show exemplary products. ! This PDF document was created based on the data listed on the TDK Corporation website.

[!] All specifications are subject to change without notice.

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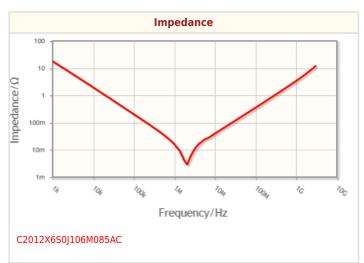


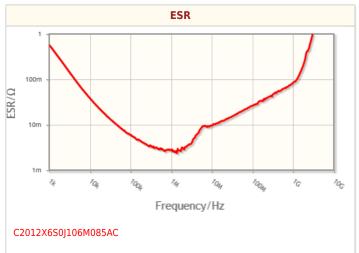


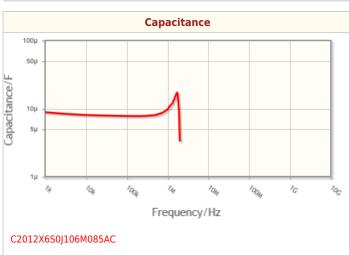


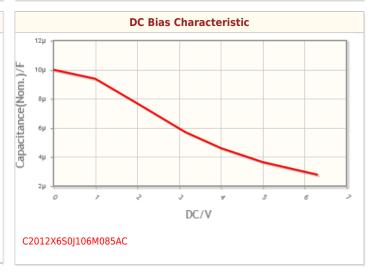


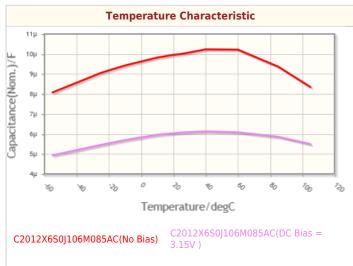
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

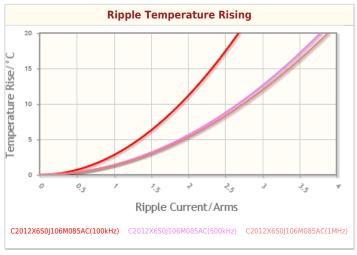












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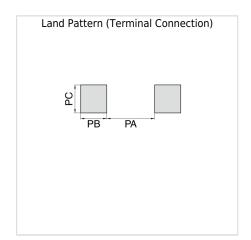








Associated Images



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