

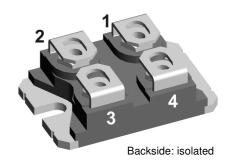
Standard Rectifier

3~ Rectifier					
V_{RRM}	=	1600 V			
I_{DAV}	=	150 A			
I _{FSM}	=	800 A			

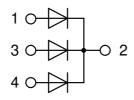
Half 3~ Bridge, Common Cathode

Part number

DMA150YC1600NA







Features / Advantages:

- Planar passivated chips
- Very low leakage current
- Very low forward voltage drop • Improved thermal behaviour

Applications:

- Diode for main rectification
- For single and three phase bridge configurations

Package: SOT-227B (minibloc)

- Isolation Voltage: 3000 V~ • Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0 Base plate: Copper
- internally DCB isolated Advanced power cycling

Terms and Conditions of Usage

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

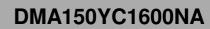
to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

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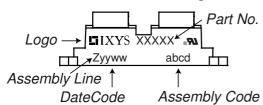
Rectifier	•			l	Ratings	S	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			1700	V
V_{RRM}	max. repetitive reverse blocking	ı voltage	$T_{VJ} = 25^{\circ}C$			1600	V
I _R	reverse current	V _R = 1600 V	$T_{VJ} = 25^{\circ}C$			100	μΑ
		$V_R = 1600 V$	$T_{VJ} = 150$ °C			1.5	mA
V _F	forward voltage drop	I _F = 50 A	$T_{VJ} = 25^{\circ}C$			1.21	V
		$I_{F} = 150 \text{ A}$				1.62	٧
		$I_F = 50 \text{ A}$	$T_{VJ} = 125$ °C			1.16	V
		$I_F = 150 \text{ A}$				1.69	٧
I DAV	bridge output current	T _C = 95°C	T _{vJ} = 150°C			150	Α
		rectangular $d = \frac{1}{3}$					i ! !
V _{F0}	threshold voltage $T_{vJ} = 150$ °C		T _{vJ} = 150°C			0.87	V
\mathbf{r}_{F}	slope resistance	loss calculation only				5.6	mΩ
R _{thJC}	thermal resistance junction to ca	ase				0.6	K/W
R _{thCH}	thermal resistance case to heat	sink			0.10		K/W
P _{tot}	total power dissipation		$T_{C} = 25^{\circ}C$			165	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			800	Α
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			865	Α
		t = 10 ms; (50 Hz), sine	T _{vJ} = 150°C			680	Α
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			735	Α
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			3.20	kA2s
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			3.12	kA2s
		t = 10 ms; (50 Hz), sine	T _{VJ} = 150°C			2.31	kA2s
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			2.25	kA2s
C	junction capacitance	$V_{R} = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		25		pF



DMA150YC1600NA

Package SOT-227B (minibloc)					Ratings			
Symbol	Definition	Conditions			min.	typ.	max.	Unit
RMS	RMS current	per terminal					150	Α
T _{VJ}	virtual junction temperature	9			-40		150	°C
Top	operation temperature	operation temperature					125	°C
T _{stg}	storage temperature				-40		150	°C
Weight						30		g
M _D	mounting torque				1.1		1.5	Nm
$\mathbf{M}_{_{\mathbf{T}}}$	terminal torque				1.1		1.5	Nm
d _{Spp/App}	oroonaga diatanaa an aurfe	terminal to terminal 10.5						mm
d _{Spb/Apb}	creepage distance on surface striking distance through air termina		terminal to backside	8.6	6.8			mm
V _{ISOL}	isolation voltage	t = 1 second			3000			٧
1002	t = 1 minute 50/60 Hz, RMS; lisoL ≤ 1 mA			2500			٧	





Part description

D = Diode M = Standard Rectifier

A = (up to 1800V)

150 = Current Rating [A]

YC = Half 3~ Bridge, Common Cathode

1600 = Reverse Voltage [V]

NA = SOT-227B (minibloc)

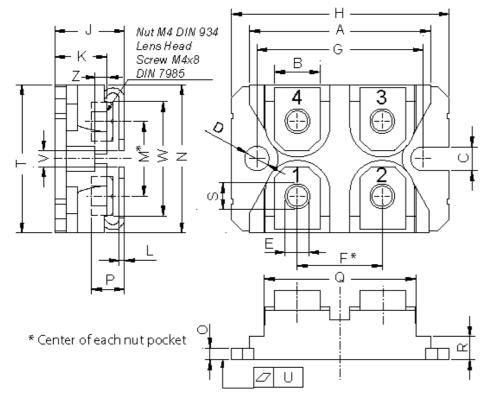
Orderin	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	DMA150YC1600NA	DMA150YC1600NA	Tube	10	509174

Similar Part	Package	Voltage class
DMA150YA1600NA	SOT-227B (minibloc)	1600

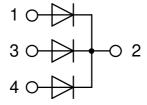
Equivalent Circuits for Simulation			* on die level	$T_{VJ} = 150 ^{\circ}\text{C}$
$I \rightarrow V_0$)— <u>R</u> o	Rectifier		
V _{0 max}	threshold voltage	0.87		V
$R_{0 \; max}$	slope resistance *	4.4		$m\Omega$



Outlines SOT-227B (minibloc)

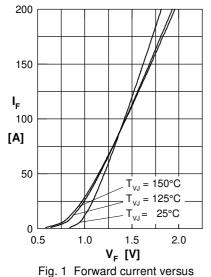


Dim.	Millimeter		Inches		
DIIII.	min	max	min	max	
Α	31.50	31.88	1.240	1.255	
В	7.80	8.20	0.307	0.323	
С	4.09	4.29	0.161	0.169	
D	4.09	4.29	0.161	0.169	
Е	4.09	4.29	0.161	0.169	
F	14.91	15.11	0.587	0.595	
G	30.12	30.30	1.186	1.193	
Н	37.80	38.23	1.488	1.505	
J	11.68	12.22	0.460	0.481	
K	8.92	9.60	0.351	0.378	
L	0.74	0.84	0.029	0.033	
M	12.50	13.10	0.492	0.516	
N	25.15	25.42	0.990	1.001	
0	1.95	2.13	0.077	0.084	
Р	4.95	6.20	0.195	0.244	
Q	26.54	26.90	1.045	1.059	
R	3.94	4.42	0.155	0.167	
S	4.55	4.85	0.179	0.191	
Т	24.59	25.25	0.968	0.994	
U	-0.05	0.10	-0.002	0.004	
V	3.20	5.50	0.126	0.217	
W	19.81	21.08	0.780	0.830	
Z	2.50	2.70	0.098	0.106	





Rectifier



voltage drop per diode

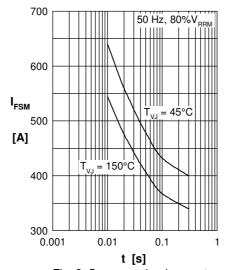


Fig. 2 Surge overload current vs. time per diode

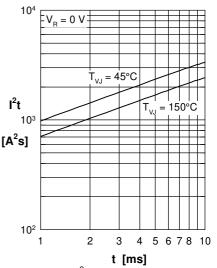


Fig. 3 $\,$ I 2 t versus time per diode

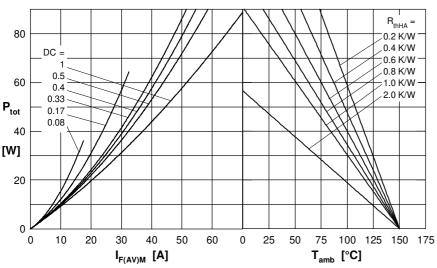


Fig. 4 Power dissipation vs. forward current and ambient temperature per diode

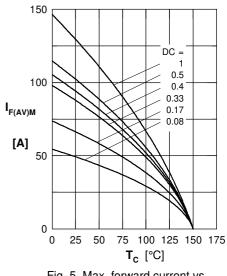


Fig. 5 Max. forward current vs. case temperature per diode

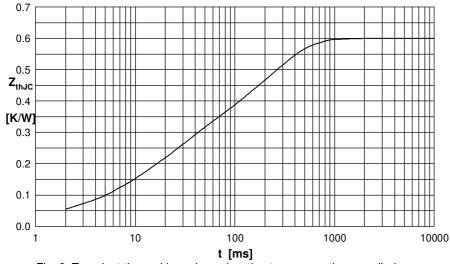


Fig. 6 Transient thermal impedance junction to case vs. time per diode

Constants for Z_{thJC} calculation:

i	$R_{thi}^{}$ (K/W)	t _i (s)
1	0.0240	0.01000
2	0.0160	0.00001
3	0.0500	0.00500
4	0.1800	0.02300
5	0.3300	0.22000