

Description

FGA65A3H is 650 V Field Stop IGBT. Sanken original trench structure decreases gate capacitance, and achieves high speed switching and switching loss reduction. Thus, Field Stop IGBT can improve the efficiency of your circuit.

Features

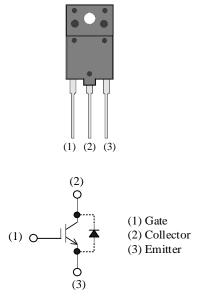
- Low Saturation Voltage
- High Speed Switching
- Bare Lead Frame: Pb-free (RoHS Compliant)
- V_{CE} ------ 650 V
- $I_C (T_C = 100 \text{ °C})$ ------15 A

Applications

• DCM and CRM PFC Circuit

Packages

TO3PF-3L



Not to scale

Absolute Maximum Ratings

Parameter	Symbol	Conditions	Rating	Unit	Remarks
Collector to Emitter Voltage	V _{CE}		650	V	
Gate to Emitter Voltage	V _{GE}		±30	V	
	т	$T_C = 25 \ ^{\circ}C$	25	A	
Continuous Collector Current	I _C	$T_{\rm C} = 100 \ ^{\circ}{\rm C}$	15	А	
Pulsed Collector Current	I _{C(PULSE)}	$\begin{array}{l} P_W \leq 1 \mbox{ ms}, \\ \mbox{duty cycle} \leq 1\% \end{array}$	90	А	
	т	$T_C = 25 \ ^{\circ}C$	0.5	А	
Diode Continuous Forward Current	$I_{\rm F}$	$T_{C} = 100 \ ^{\circ}C$	0.2	А	
Diode Pulsed Forward Current	I _{F(PULSE)}	$P_W \le 1 \text{ ms},$ duty cycle $\le 1\%$	2	А	
Short Circuit Withstand Time	t _{SC}	$V_{GE} = 15 \text{ V},$ $V_{CE} = 400 \text{ V},$ $T_J = 175 ^{\circ}\text{C}$	10	μs	
Power Dissipation	P _D	$T_C = 25 \ ^{\circ}C$	72	W	
Operating Junction Temperature	T _J		175	°C	
Storage Temperature	T _{STG}		-55 to 150	°C	
Isolation Voltage	V _{ISO(RMS)}	Between surface of case and each pin; AC, 60 Hz, 1 min	1500	v	

Unless otherwise specified, $T_{A} = 25 \ ^{\circ}C$.

Thermal Characteristics

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	Remarks
Thermal Resistance (Junction to Case)	$R_{ heta JC}$				2.08	°C/W	

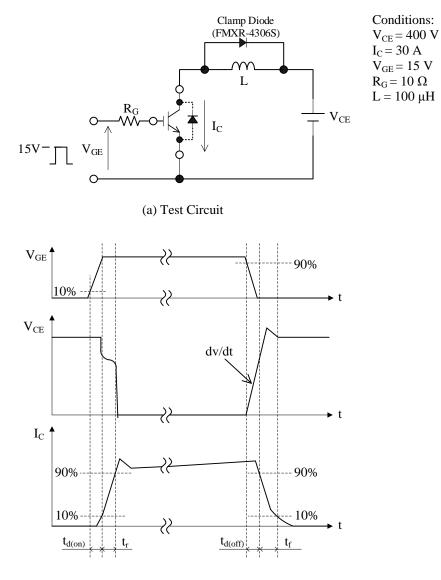
Electrical Characteristics

Unless	otherwise	specified,	$T_{\Lambda} =$	25 °C.
Onicos	other wise	specificu,	IA -	25 C.

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Collector to Emitter Breakdown Voltage	V _{(BR)CES}	$I_{C} = 100 \ \mu A, V_{GE} = 0 \ V$	650			V	
Collector to Emitter Leakage Current	I _{CES}	$V_{CE} = 650 \text{ V}, V_{GE} = 0 \text{ V}$	—	_	100	μΑ	
Gate to Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30 \text{ V}$		_	±500	nA	
Gate Threshold Voltage	V _{GE(TH)}	$V_{CE} = 10 \text{ V}, I_C = 1 \text{ mA}$	4.0	5.5	7.0	V	
Collector to Emitter Saturation Voltage	V _{CE(sat)}	$V_{GE} = 15 \text{ V}, I_C = 30 \text{ A}$		1.9	2.37	V	
Input Capacitance	C _{ies}	$V_{CE} = 20 V,$		1800			
Output Capacitance	C _{oes}	$V_{GE} = 0 V,$		200		pF	
Reverse Transfer Capacitance	C _{res}	f = 1.0 MHz		80			
Gate Charge	Q_{g}	$V_{CE} = 520 \text{ V}, I_C = 30 \text{ A},$ $V_{GE} = 15 \text{ V}$	_	60		nC	
Turn-on Delay Time	t _{d(on)}			30	—		
Rise Time	t _r			30	—	ne	
Turn-off Delay Time	$t_{d(\mathrm{off})}$	$T_{J} = 25 \ ^{\circ}C;$		90		ns	
Fall Time	t _f	see Figure 1		30			
Turn-on Energy ⁽¹⁾	Eon			0.5		mJ	
Turn-off Energy	E_{off}			0.4		1113	
Turn-on Delay Time	t _{d(on)}			30			
Rise Time	t _r			30		ns	
Turn-off Delay Time	$t_{d(off)}$	T _I = 175 °C;		120	—		
Fall Time	$t_{\rm f}$	see Figure 1		60			
Turn-on Energy ⁽¹⁾	Eon			0.8		mJ	
Turn-off Energy	$E_{\rm off}$			0.7		1113	
Emitter to Collector Diode Forward Voltage	$V_{\rm F}$	$I_F = 0.5 A$	_	2.0		V	

⁽¹⁾ Energy losses include the reverse recovery of diode.

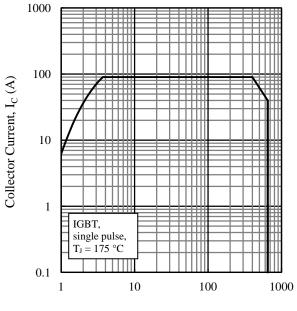
Test Circuits and Waveforms



(b) Waveform

Figure 1. Test Circuits and Waveforms of dv/dt and Switching Time

Rating and Characteristic Curves



Collector-Emitter Voltage, V_{CE} (V)

Figure 2. IGBT Reverse Bias Safe Operating Area

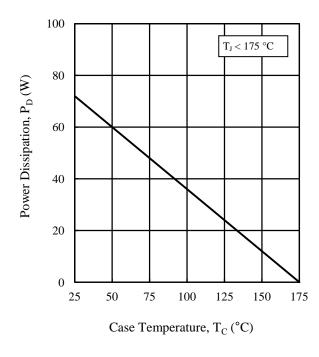


Figure 4. Power Dissipation vs. Case Temperature

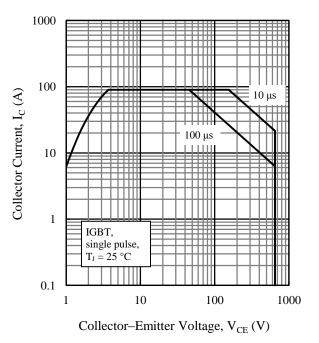


Figure 3. IGBT Safe Operating Area

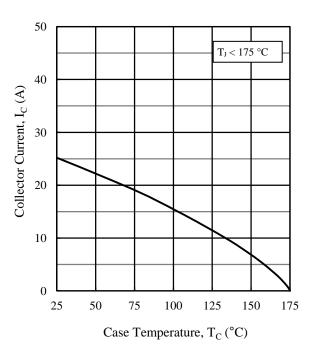


Figure 5. Collector Current vs. Case Temperature

FGA65A3H

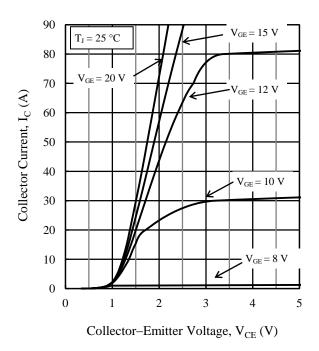


Figure 6. Output Characteristics ($T_J = 25 \ ^{\circ}C$)

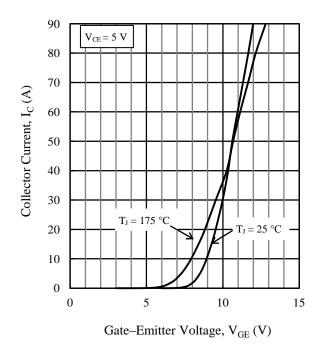


Figure 8. Transfer Characteristics

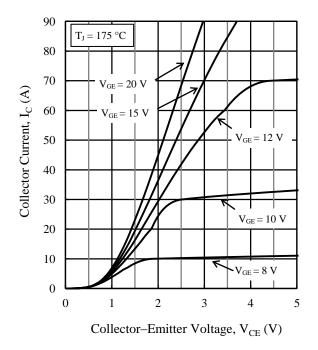


Figure 7. Output Characteristics ($T_J = 175 \ ^{\circ}C$)

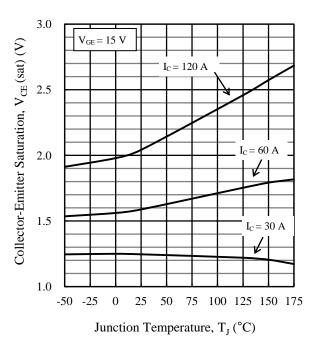


Figure 9. Saturation Voltage vs. Junction Temperature

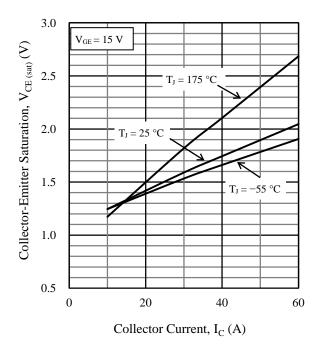


Figure 10. Saturation Voltage vs. Collector Current

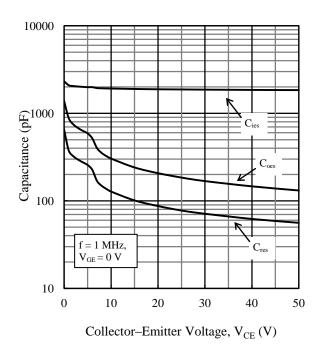


Figure 12. Capacitance Characteristics

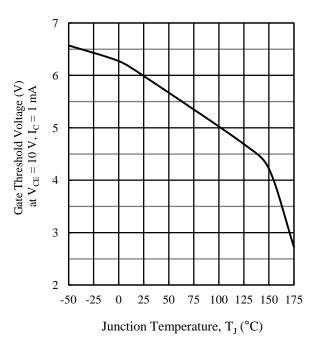


Figure 11. Gate Threshold Voltage vs. Junction Temperature

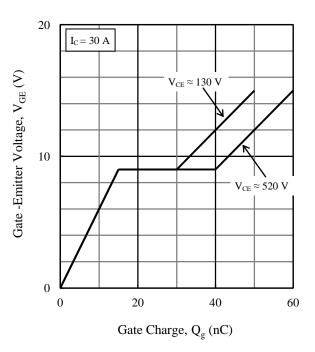


Figure 13. Typical Gate Charge

FGA65A3H

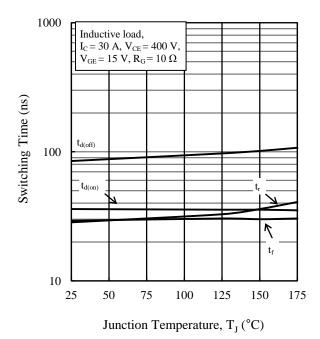


Figure 14. Switching Time vs. Junction Temperature

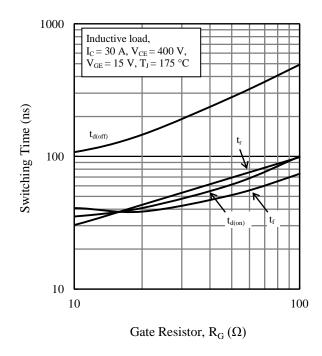


Figure 16. Switching Time vs. Gate Resistor

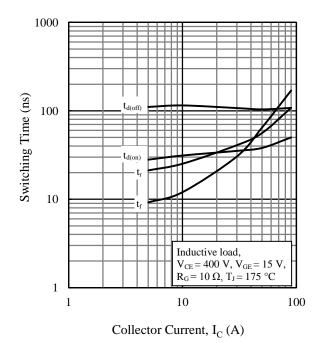


Figure 15. Switching Time vs. Collector Current

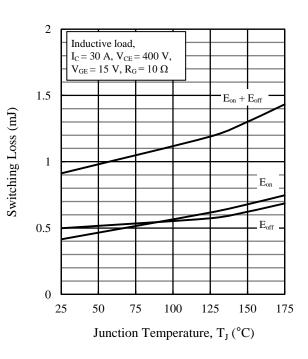


Figure 17. Switching Loss vs. Junction Temperature

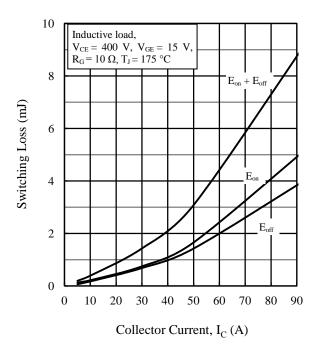


Figure 18. Switching Loss vs. Collector Current

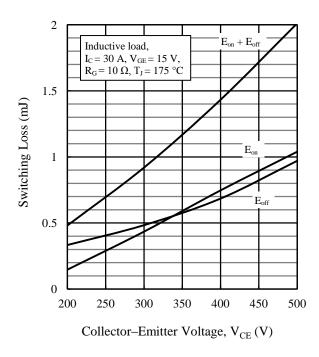


Figure 20. Switching Loss vs. Collector–Emitter Voltage

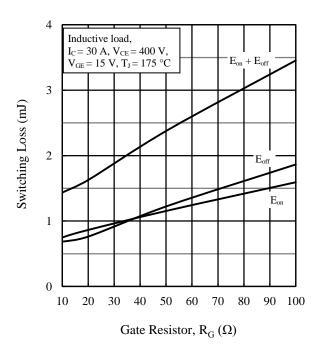


Figure 19. Switching Loss vs. Gate Resistor

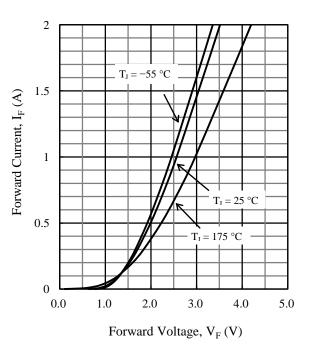


Figure 21. Diode Forward Characteristics

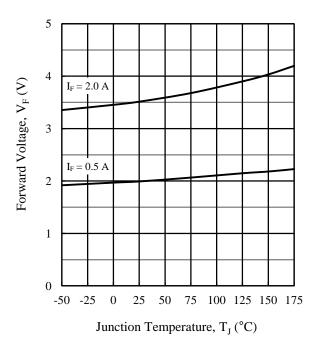


Figure 22. Diode Forward Voltage vs. Junction Temperature

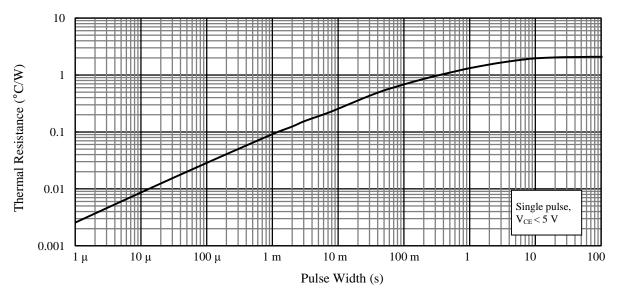
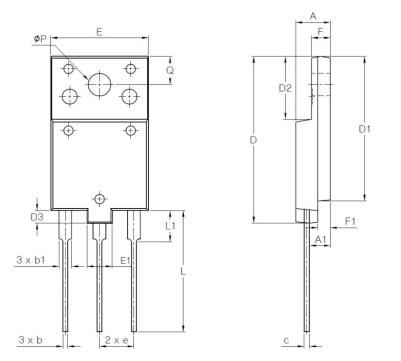


Figure 23. Transient Thermal Resistance

Physical Dimensions

• TO3PF-3L

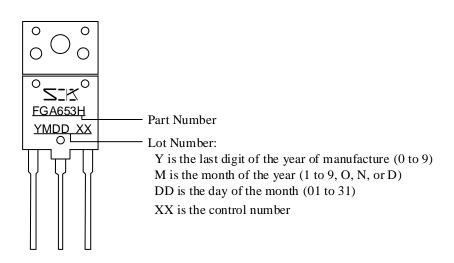


SYMBOL	MIN	NOM	MAX
A	5.30	5.50	5.70
A1	3.10	3.30	3.50
b	0.65	0.75	0.95
b1	1.80	2.00	2.20
С	0.80	0.90	1.10
D	26.30	26.50	26.70
D1	22.80	23.00	23.20
D2	9.80	10.00	10.20
D3	1.80	2.00	2.20
E	15,30	15.50	15.70
E1	3.80	4.00	4.20
е	5.25	5.45	5.65
F	2.80	3.00	3.20
F1	1.80	2.00	2.20
L	19.10	19.30	19.50
L1	4.80	5.00	5.20
Q	4.30	4.50	4.70
φP	3.40	3.60	3.80

NOTES:

- Dimensions in millimeters
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, be sure to minimize the working time within the following limits: Flow: 260 ± 5 °C / 10 ± 1 s, 2 times Soldering iron: 380 ± 10 °C / 3.5 ± 0.5 s, 1 time (Soldering should be at a distance of at least 1.5 mm from the body of the products.)
- Recommended screw torque: 0.686 N·m to 0.882 N·m (7 kgf·cm to 9 kgf·cm)

Marking Diagram



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