

## isc Thyristors

**ISCI29**

### DESCRIPTION

- With TO-92 package
- Sensitive gate trigger current
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATIONS

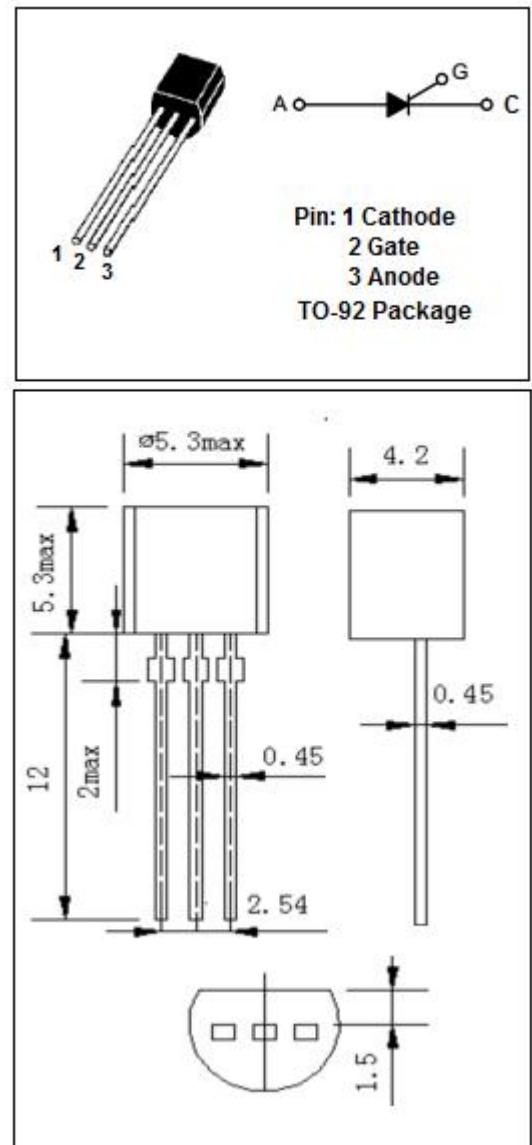
- Designed for high volume, line-powered consumer application such as relay and lamp drivers, small motor controls, gate drivers for larger thyristors, and sensing and detection circuits

### ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

| SYMBOL       | PARAMETER                                       | VALUE   | UNIT |
|--------------|---|---------|------|
| $V_{DRM}$    | Repetitive peak off-state voltage               | 600     | V    |
| $V_{RRM}$    | Repetitive peak off-state voltage               | 600     | V    |
| $I_{T(RMS)}$ | RMS on-state current(180° conduction angle)     | 0.8     | A    |
| $I_{TSM}$    | Non-repetitive peak on-state current((tp=10ms)) | 10      | A    |
| $I_{GM}$     | Peak gate current((tp=20 μ s))                  | 1       | A    |
| $P_{GM}$     | Peak gate power                                 | 0.1     | W    |
| $P_{G(AV)}$  | Average gate power                              | 0.01    | W    |
| $T_j$        | Operating junction temperature                  | -40-125 | °C   |
| $T_{stg}$    | Storage temperature range                       | -40-150 | °C   |

### THERMAL RESISTANCE

| SYMBOL         | PARAMETER        | MAX | UNIT |
|----------------|------------------|-----|------|
| $R_{th} (j-c)$ | Junction to case | 75  | °C/W |



**isc Thyristors****ISCI29****ELECTRICAL CHARACTERISTICS ( $T_c=25^\circ\text{C}$  unless otherwise specified)**

| SYMBOL    | PARAMETER                         | CONDITIONS  | MIN | TYP | MAX       | UNIT           |
|-----------|-----------------------------------|---|-----|-----|-----------|----------------|
| $I_{RRM}$ | Repetitive peak reverse current   | $V_R = V_{RRM}$<br>$V_R = V_{RRM}; T_j = 125^\circ\text{C}$ |     |     | 10<br>100 | $\mu\text{ A}$ |
| $I_{DRM}$ | Repetitive peak off-state current | $V_D = V_{DRM}$<br>$V_D = V_{DRM}; T_j = 125^\circ\text{C}$ |     |     | 10<br>100 | $\mu\text{ A}$ |
| $I_{GT}$  | Gate trigger current              | $V_D = 8\text{V}; R_L = 100\Omega$                          |     |     | 200       | $\mu\text{ A}$ |
| $V_{TM}$  | On-state voltage                  | $I_T = 1\text{A}, t_p = 380\mu\text{s}$                     |     |     | 1.6       | V              |
| $I_H$     | Holding current                   | $I_T = 0.1\text{A}, \text{Gate Open}$                       |     |     | 5         | mA             |
| $V_{GT}$  | Gate trigger voltage              | $V_D = 8\text{V}; R_L = 100\Omega$                          |     |     | 1         | V              |

