## LA4282

# **Audio Power Amplifier** Class AB, 10W × 2-Channel

#### Overview

The LA4282 is the IC for 2-channel SE power amplifier that is developed for TVs and monitors audio system. High power and low distortion are realized. This IC incorporate various functions (muting function, and various protection circuit) necessary for audio system.

#### **Features**

- High-power 2-channel AF power amplifier
- Low distortion
- Minimum number of external parts required (no bootstrap capacitor required)
- Low pop noise at the time of power supply ON/OFF
- Good ripple rejection (58dB typ)
- Wide operating voltage range
- External muting available
- On-chip protector against abnormality (thermal shutdown, overvoltage)

#### **Typical Applications**

- Home Stereo

#### **SPECIFICATION**

#### **ABSOLUTE MAXIMUM RATINGS** at $Ta = 25^{\circ}C$ (Note 1)

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max	Quiescent	45	V
Maximum output current	I <sub>O</sub> peak		4	Α
Allowable power dissipation	Pd max	With heat sink	25	W
Operating temperature	Topr		−20 to +75	°C
Storage temperature	Tstg		-40 to +150	°C

<sup>1.</sup> Stresses exceeding those listed in the Absolute Maximum Rating table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

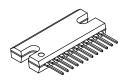
#### **RECOMMENDED OPERATING RANGE** at Ta = 25°C (Note 2)

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>		32	٧
	V <sub>CC op</sub>		10 to 40	٧
Recommended load resistance	Rı		8	Ω

<sup>2.</sup> Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

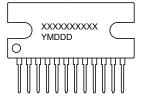


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SIP12 26.8 mm x 11.8 mm

#### **GENERIC MARKING DIAGRAM\***



Y = Year M = Month DDD = Additional Traceability Data

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

#### **ORDERING INFORMATION**

Ordering Code: LA4282-E

Package (Pb-Free / Halogen Free)

Shipping (Qty / packing) 20 / Fan-Fold

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

http://www.onsemi.com/pub\_link/Collateral/BRD8011-D.PDF

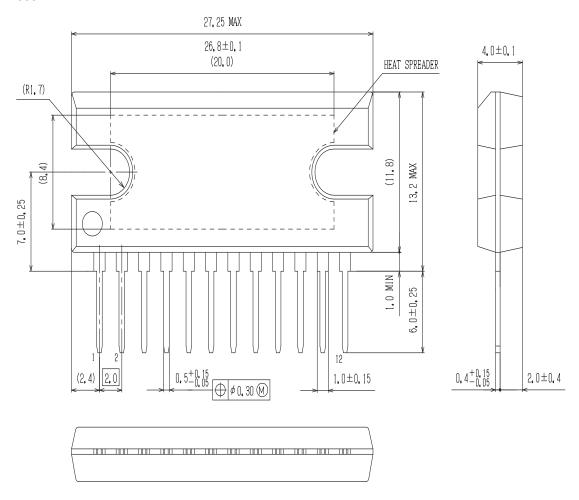
Parameter	Symbol	Conditions	Ratings			1.1
Parameter		Conditions	min	typ	max	Unit
Quiescent current	ICCO (1)	Quiescent	30	60	100	mA
	ICCO (2)	Muting switch On	30	56	100	mA
Voltage gain	VG		38	40	42	dB
Voltage gain difference	'VG				1.5	dB
Output power	P <sub>O</sub> (1)	THD = 1%	9.0	10.0		W
	P <sub>O</sub> (2)	THD = 3%	10.0	11.5		W
Total harmonic distortion	THD	P <sub>O</sub> = 2W		0.05	0.20	%
Output noise voltage	V <sub>NO</sub>	Rg = 10k:, BW = 20Hz to 20kHz		0.25	1.0	mV
Ripple rejection	SVRR	$Rg = 10k : , f_R = 100Hz, V_R = 0dBm$	45	58		dB
Crosstalk	СТ	Rg = 10k:	45	60		dB
Muting	V <sub>O (MT)</sub>	Muting switch On, V <sub>IN</sub> = 5dBm			35	dBm

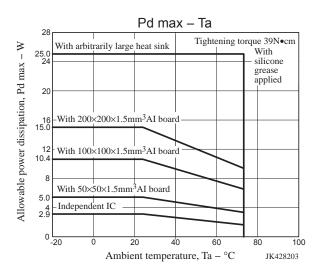
<sup>3.</sup> Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## PACKAGE DIMENSIONS

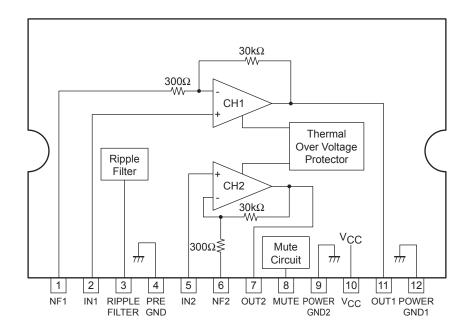
unit: mm

SIP12 26.8x11.8 / SIP12H CASE 127AM ISSUE A

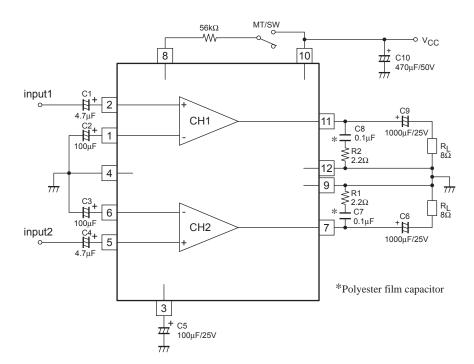


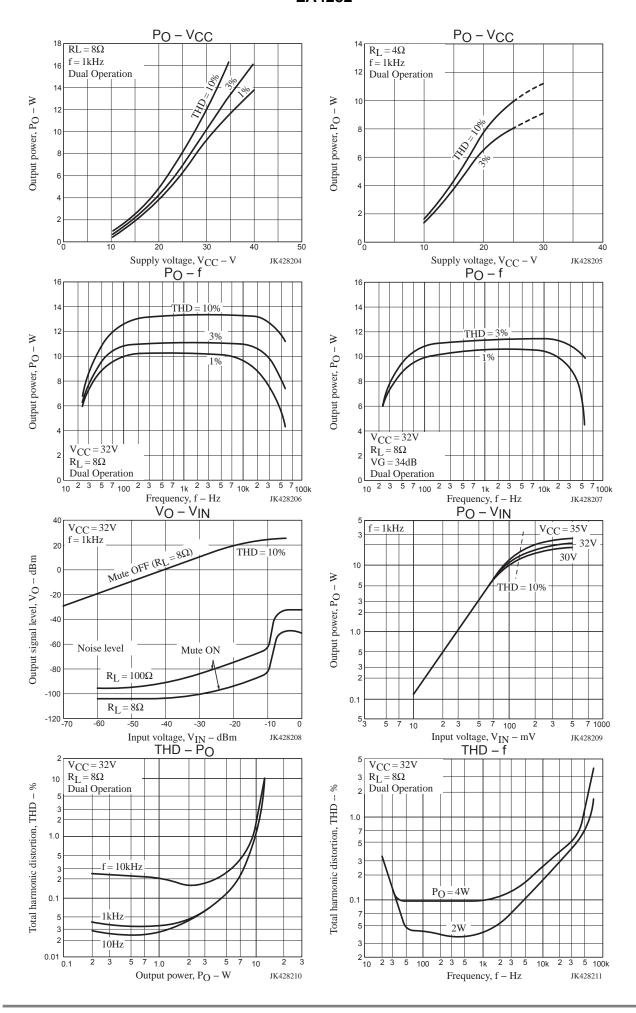


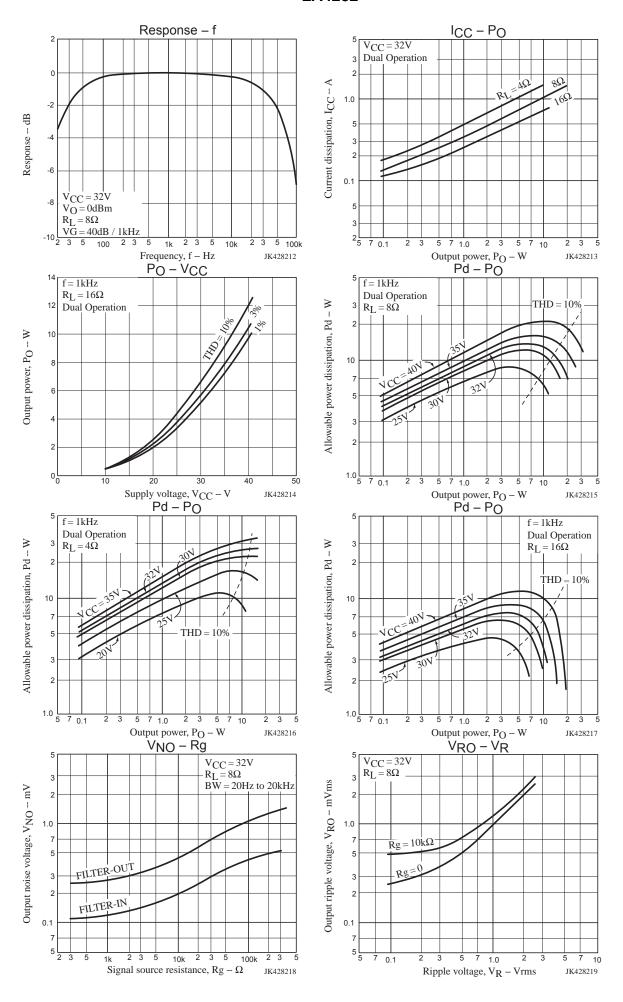
## Pin Assignment and Block Diagram



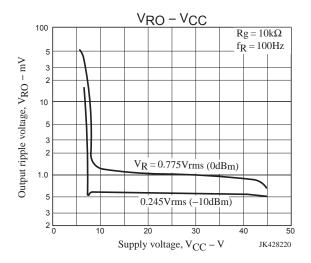
# **Test Circuit**

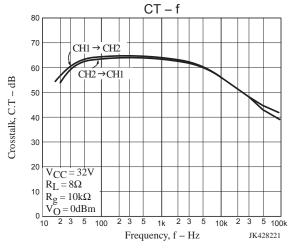






### **LA4282**





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