

4V Drive Nch MOSFET

RSJ650N10

Structure

Silicon N-channel MOSFET

Features

- 1) Low on-resistance.
- 2) High power package.
- 3) 4V drive.

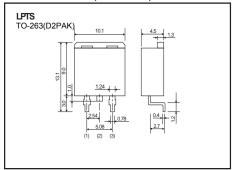
Application

Switching

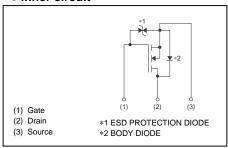
Packaging specifications

	Package	Taping	
Type	Code	TL	
	Basic ordering unit (pieces)	1000	
RSJ650N1	0		

Dimensions (Unit : mm)



• Inner circuit



● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		V_{DSS}	100	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	Continuous	I _D *3	±65	Α
	Pulsed	I _{DP} *1	±130	Α
Source current	Continuous	I _S *3	65	Α
(Body Diode)	Pulsed	I _{SP} *1	130	Α
Power dissipation		P _D *2	100	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C
	•	•		

^{*1} $P_W \le 10 \mu s$, Duty cycle $\le 1\%$

• Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Case	Rth (ch-c)*	1.25	°C/W

^{*} T_C=25°C

^{*2} T_C=25°C

^{*3} Please use within the range of SOA.

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	1	-	±10	μA	$V_{GS}=\pm20V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	100	-	-	٧	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	1	-	1	μA	V _{DS} =100V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	1	-	2.5	٧	V_{DS} =10V, I_{D} =1mA
Static drain-source on-state	R _{DS (on)} *	1	6.5	9.1	mΩ	I _D =32.5A, V _{GS} =10V
resistance	DS (on)	1	7	9.8	11177	$I_D = 32.5A, V_{GS} = 4V$
Forward transfer admittance	ΙΥ _{fs} Γ΄	45	-	-	S	V_{DS} =10V, I_{D} =32.5A
Input capacitance	C _{iss}	ı	10780	-	pF	V _{DS} =25V
Output capacitance	C _{oss}	-	785	-	pF	V _{GS} =0V
Reverse transfer capacitance	C_{rss}	-	560	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	1	45	-	ns	V _{DD} ≒50V, I _D =32.5A
Rise time	t _r *	1	170	-	ns	V _{GS} =10V
Turn-off delay time	t _{d(off)} *	1	640	-	ns	$R_L=1.54\Omega$
Fall time	t _f *	1	480	-	ns	$R_G=10\Omega$
Total gate charge	Q _g *	-	260	-	nC	V _{DD} ≒50V, I _D =32.5A
Gate-source charge	Q _{gs} *	-	24	-	nC	V _{GS} =10V
Gate-drain charge	Q _{gd} *	-	60	-	nC	

^{*}Pulsed

●Body diode characteristics (Source-Drain)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	1.5	V	I _s =65A, V _{GS} =0V

^{*}Pulsed

●Electrical characteristic curves (Ta=25°C)

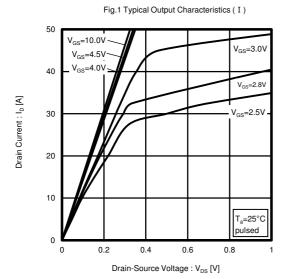


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

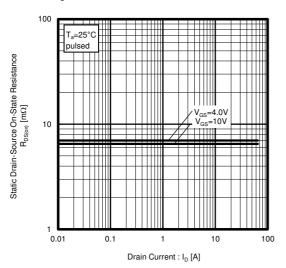


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

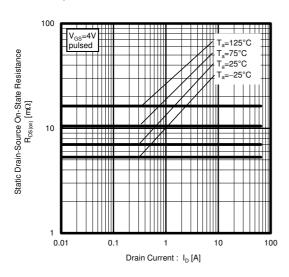


Fig.2 Typical Output Characteristics (II)

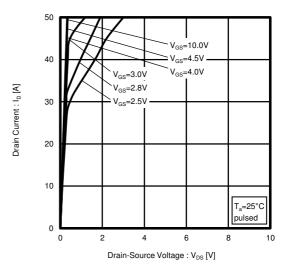


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

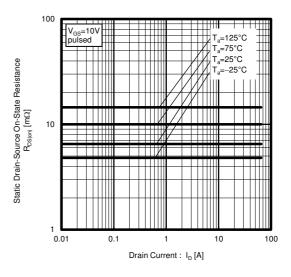


Fig.6 Forward Transfer Admittance vs. Drain Current

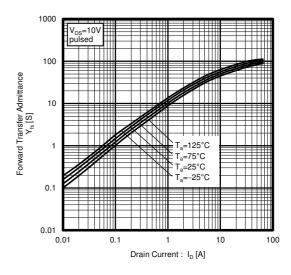
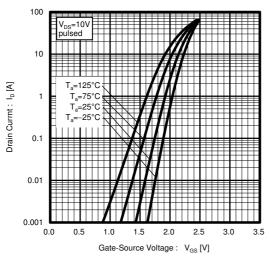
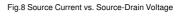


Fig.7 Typical Transfer Characteristics





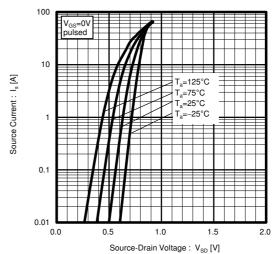


Fig.9 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

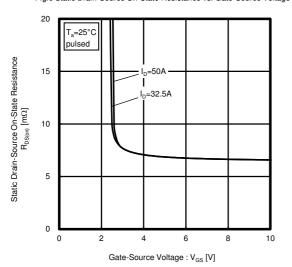


Fig.10 Switching Characteristics

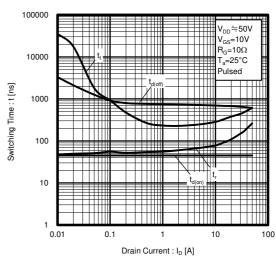


Fig.11 Dynamic Input Characteristics

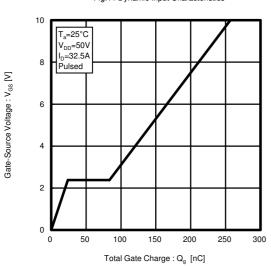


Fig.12 Typical Capacitance vs. Drain-Source Voltage

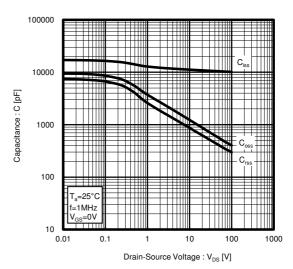


Fig.13 Normalized Transient Thermal Resistance v.s. Pulse Width

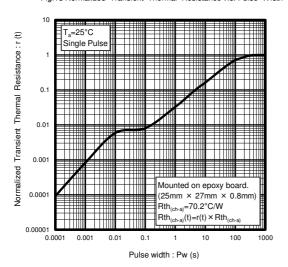
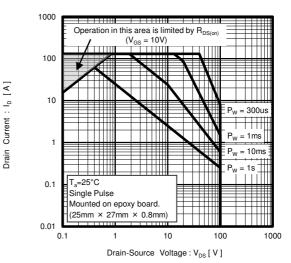


Fig.14 Maximum Safe Operating Area



Data Sheet

Measurement circuits

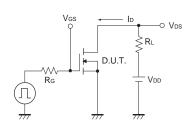


Fig.1-1 Switching Time Measurement Circuit

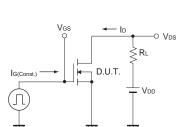


Fig.2-1 Gate Charge Measurement Circuit

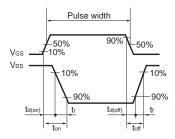


Fig.1-2 Switching Waveforms

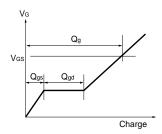


Fig.2-2 Gate Charge Waveform

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