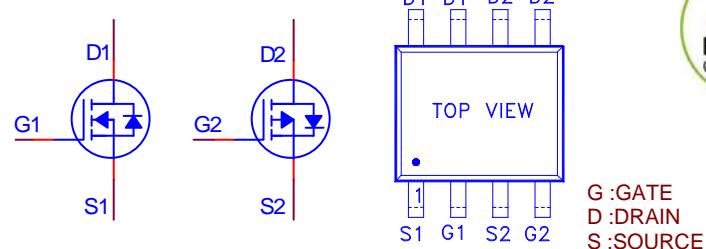


NIKO-SEM**N- & P-Channel Enhancement Mode
Field Effect Transistor****P2402OV**
SOP-8
Halogen-Free & Lead-Free**PRODUCT SUMMARY**

	$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
N-Channel	20	24mΩ	10A
P-Channel	-20	43mΩ	-5.2A

**ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS	SYMBOL	N-Channel	P-Channel	UNITS
Drain-Source Voltage	V_{DS}	20	-20	V
Gate-Source Voltage	V_{GS}	± 12	± 12	V
Continuous Drain Current	I_D	10	-5.2	A
		6.3	-3.2	
Pulsed Drain Current ¹	I_{DM}	40	-21	
Power Dissipation	P_D	2.5	1.6	W
Junction & Storage Temperature Range	T_j, T_{stg}	-55 to 150		°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		50	°C / W

¹Pulse width limited by maximum junction temperature.²Duty cycle ≤ 1%

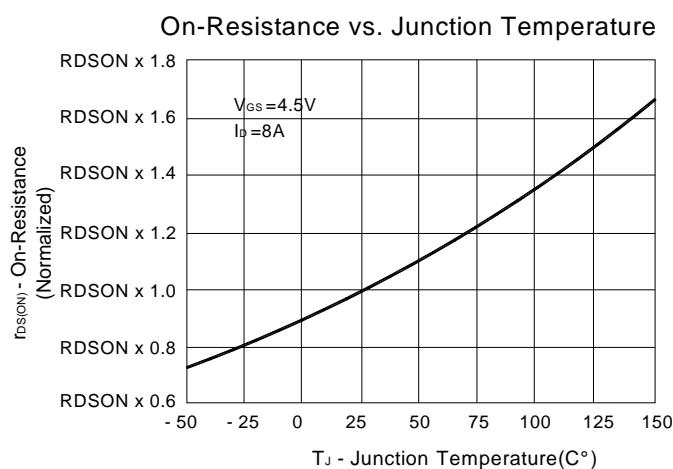
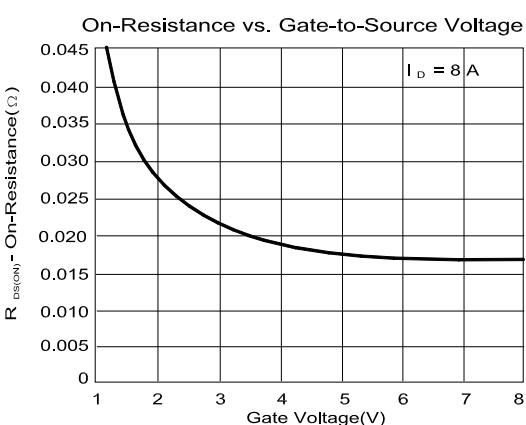
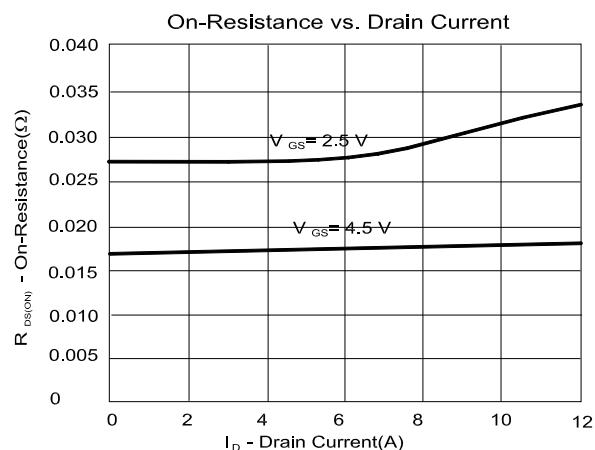
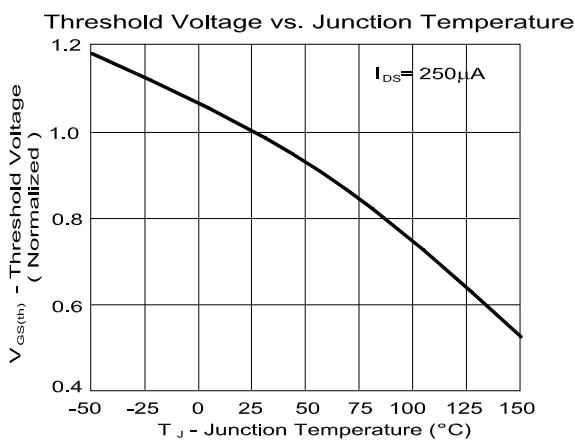
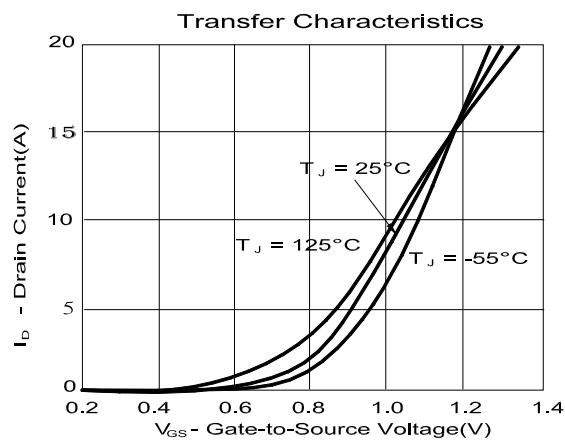
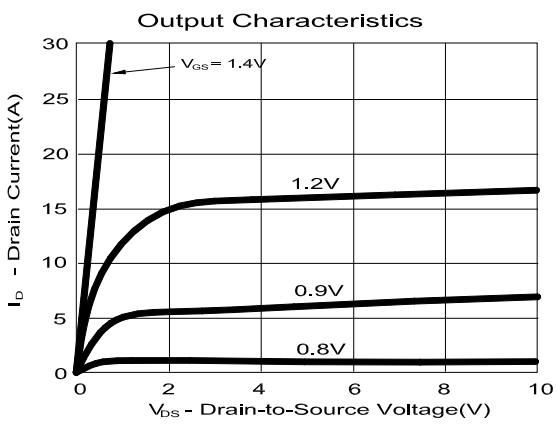
NIKO-SEM**N- & P-Channel Enhancement Mode
Field Effect Transistor****P2402OV**
SOP-8
Halogen-Free & Lead-Free**ELECTRICAL CHARACTERISTICS (T_C = 25 °C, Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	N-Ch	20		V
		V _{GS} = 0V, I _D = -250μA	P-Ch	-20		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	N-Ch	0.4	0.8	1.2
		V _{DS} = V _{GS} , I _D = -250μA	P-Ch	-0.4	-0.8	-1.2
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±12V	N-Ch			±100
		V _{DS} = 0V, V _{GS} = ±12V	P-Ch			±100
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 16V, V _{GS} = 0V	N-Ch			1
		V _{DS} = -16V, V _{GS} = 0V	P-Ch			-1
		V _{DS} = 16V, V _{GS} = 0V, T _J = 55 °C	N-Ch			10
		V _{DS} = -16V, V _{GS} = 0V, T _J = 55 °C	P-Ch			-10
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 2.5V, I _D = 5.2A	N-Ch		28	36
		V _{GS} = -2.5V, I _D = -4A	P-Ch		47	68
		V _{GS} = 4.5V, I _D = 8A	N-Ch		18	24
		V _{GS} = -4.5V, I _D = -5A	P-Ch		32	43
DYNAMIC						
Input Capacitance	C _{iss}	N-Channel V _{GS} = 0V, V _{DS} = 10V, f = 1MHz	N-Ch		732	pF
Output Capacitance	C _{oss}		P-Ch		1110	
Reverse Transfer Capacitance	C _{rss}	P-Channel V _{GS} = 0V, V _{DS} = -10V, f = 1MHz	N-Ch		241	pF
Total Gate Charge ²	Q _g		P-Ch		242	
Gate-Source Charge ²	Q _{gs}	N-Channel V _{DS} = 10V, V _{GS} = 4.5V, I _D = 5A P-Channel V _{DS} = -10V, V _{GS} = -4.5V, I _D = -5A	N-Ch		169	nC
Gate-Drain Charge ²	Q _{gd}		P-Ch		173	

NIKO-SEM**N- & P-Channel Enhancement Mode
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Turn-On Delay Time ²	$t_{d(on)}$	N-Channel $V_{DS} = 10V$, $I_D \geq 1A$, $V_{GS} = 4.5V$, $R_{GEN} = 10\Omega$ P-Channel $V_{DS} = -4V$, $I_D \geq -1A$, $V_{GS} = -4.5V$, $R_{GEN} = 10\Omega$	N-Ch	6			
Rise Time ²	t_r		P-Ch	23			
Turn-Off Delay Time ²	$t_{d(off)}$		N-Ch	5			nS
Fall Time ²	t_f		P-Ch	45			
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_C = 25^\circ C$)							
Continuous Current	I_S		N-Ch			1.9	
			P-Ch			-1.9	
Pulsed Current ³	I_{SM}		N-Ch			7.6	A
			P-Ch			-9	
Forward Voltage ¹	V_{SD}	$I_F = 1A$, $V_{GS} = 0V$ $I_F = -1A$, $V_{GS} = 0V$	N-Ch			1.3	
			P-Ch			-1.3	V

¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.³Pulse width limited by maximum junction temperature.

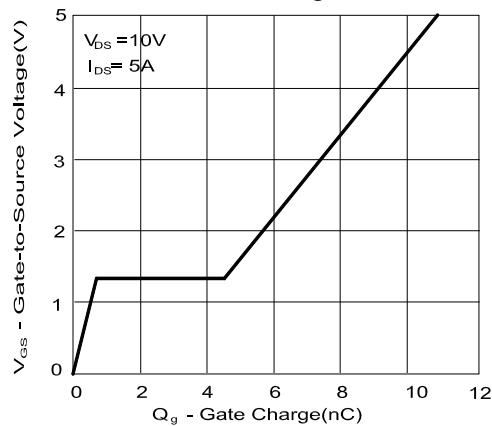
NIKO-SEM**N- & P-Channel Enhancement Mode
Field Effect Transistor****P2402OV
SOP-8
Halogen-Free & Lead-Free****N-CHANNEL**

NIKO-SEM

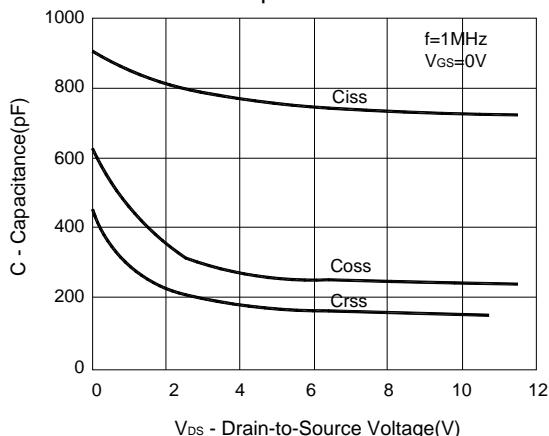
**N- & P-Channel Enhancement Mode
Field Effect Transistor**

P2402OV
SOP-8
Halogen-Free & Lead-Free

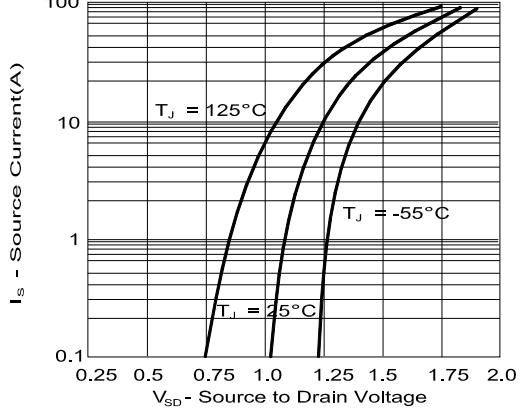
Gate Charge



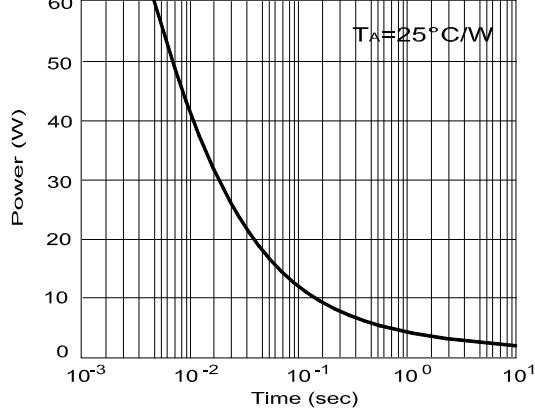
Capacitance



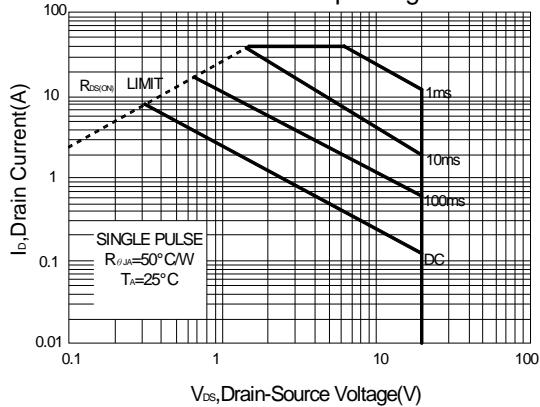
Source-Drain Diode Forward Voltage



Single Pulse Power



Maximum Safe Operating Area.

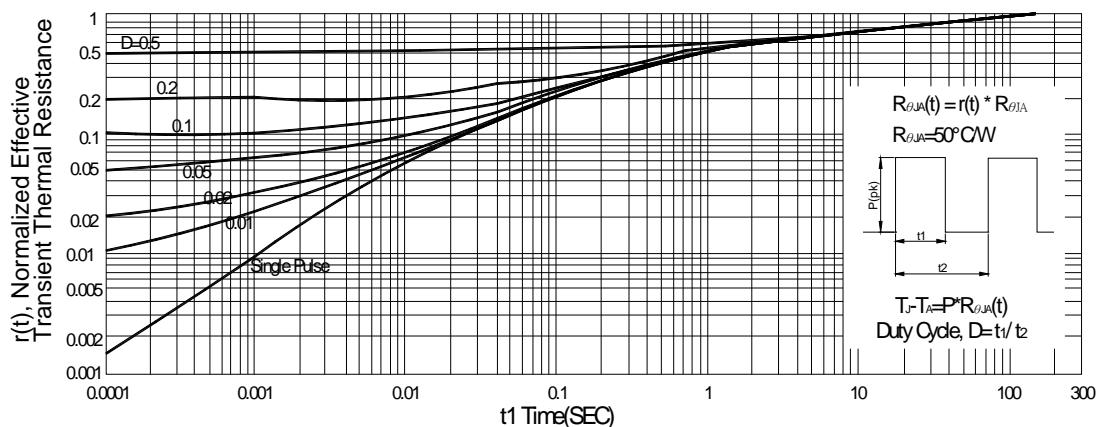


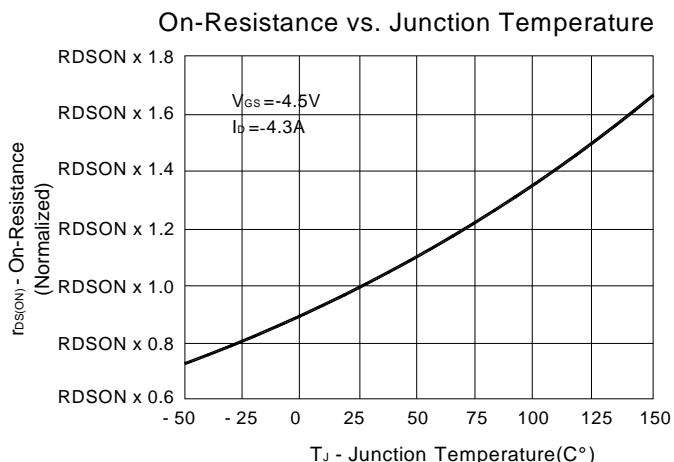
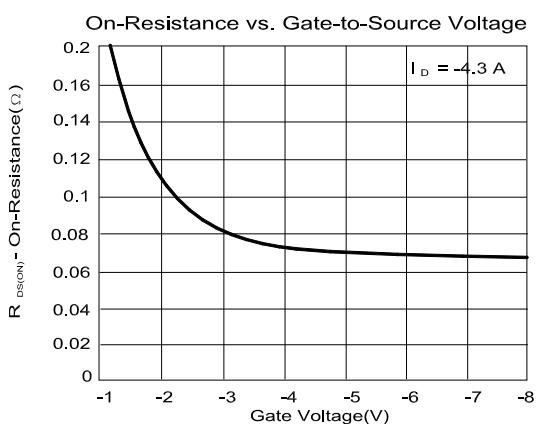
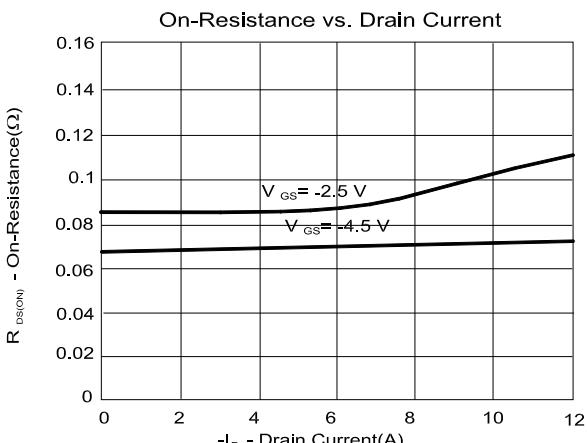
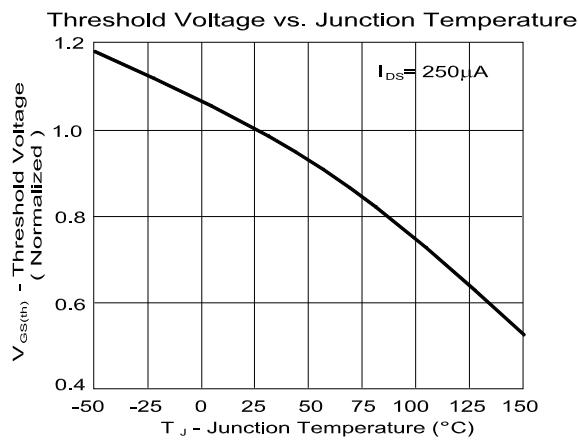
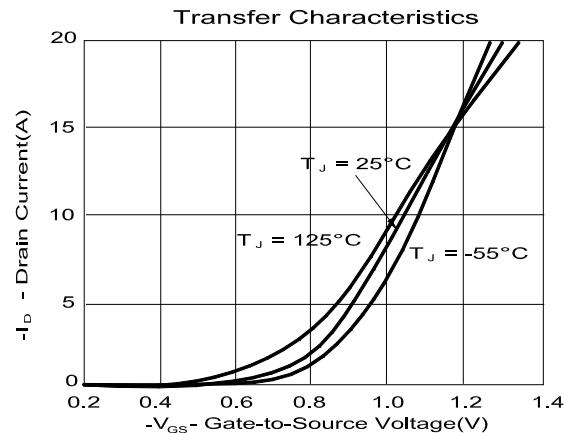
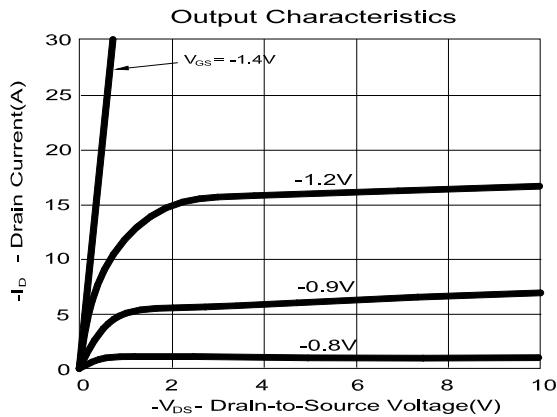
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Field Effect Transistor**

P2402OV
SOP-8
Halogen-Free & Lead-Free

Transient Thermal Response Curve.



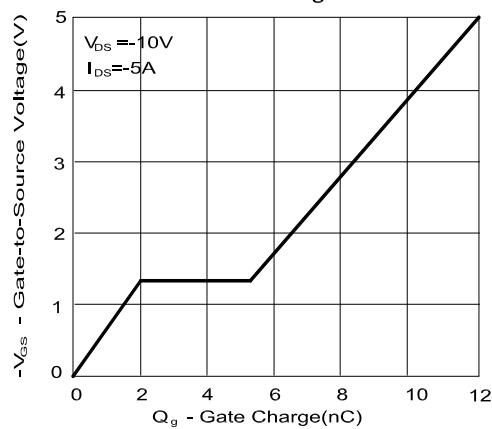
NIKO-SEM**N- & P-Channel Enhancement Mode
Field Effect Transistor****P2402OV
SOP-8
Halogen-Free & Lead-Free****P-CHANNEL**

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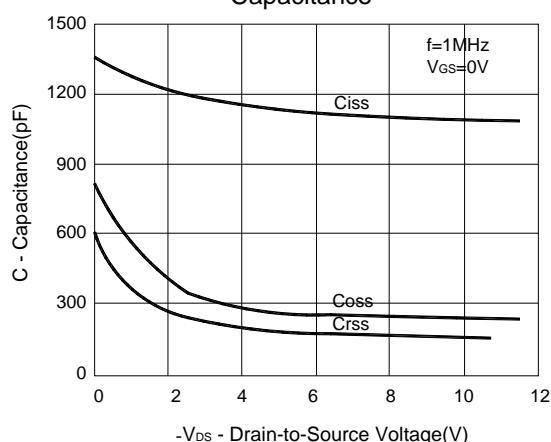
**N- & P-Channel Enhancement Mode
Field Effect Transistor**

P2402OV
SOP-8
Halogen-Free & Lead-Free

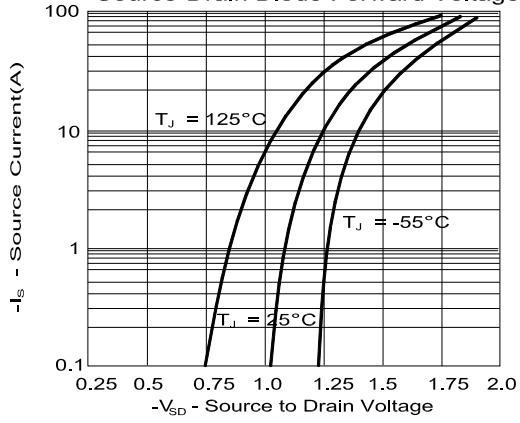
Gate Charge



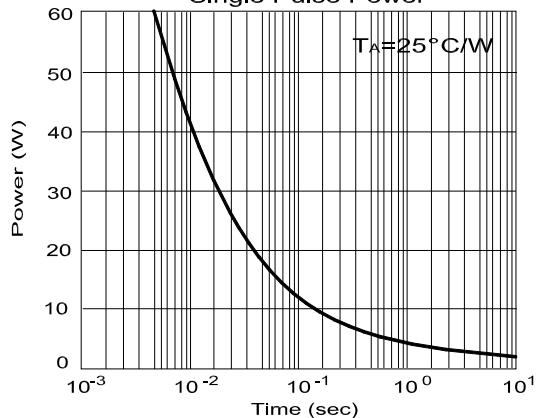
Capacitance



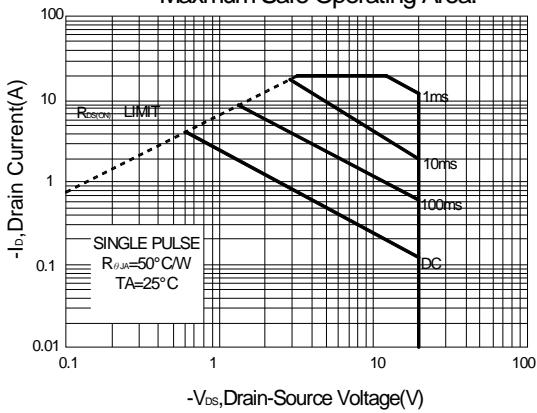
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NIKO-SEM

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