# Octal bus transceiver; 3-state Rev. 2 — 8 November 2016

Product data sheet

#### **General description** 1.

The 74AHCV245A is an 8-bit transceiver with 3-state outputs and Schmitt trigger inputs. The device features an output enable (OE) and send/receive (DIR) for direction control. A HIGH on OE causes the outputs to assume a high-impedance OFF-state.

Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

The data (An, Bn) and control (OE and DIR) inputs include Schmitt trigger inputs. These inputs can of transform slowly changing input signals into sharply defined, jitter-free output signals.

This device is ideal for driving bus lines or buffer memory address registers. It features inputs and outputs on opposite sides of the package to facilitate printed circuit board layout.

This device is fully specified for partial Power-down applications using I<sub>OFF</sub>. The I<sub>OFF</sub> circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

#### 2. Features and benefits

- Wide supply voltage range from 1.8 V to 5.5 V
- Typical t<sub>pd</sub> of 3.2 ns at 5 V
- Typical V<sub>OL(p)</sub> < 0.8 V at V<sub>CC</sub> = 3.3 V, T<sub>amb</sub> = 25 °C
- Typical V<sub>OH(v)</sub> > 2.3 V at V<sub>CC</sub> = 3.3 V, T<sub>amb</sub> = 25 °C
- Supports mixed-mode voltage operation on all ports
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
  - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3 kV
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 2 kV
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

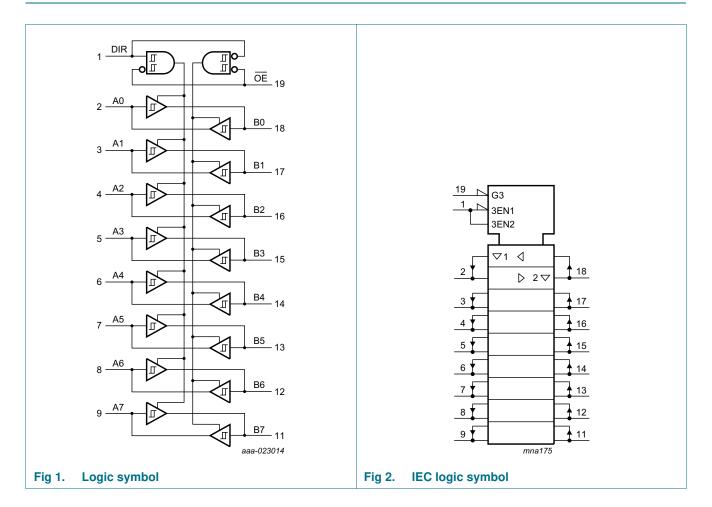
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#### **Ordering information** 3.

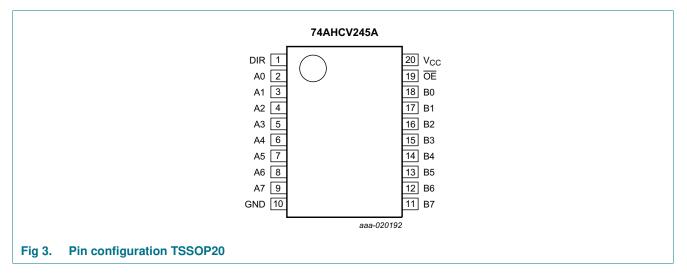
Table 1. Ordering information					
Type number Package					
	Temperature range	Name	Description	Version	
74AHCV245APW	–40 °C to +125 °C	TSSOP20	plastic thin shrink small outline package; 20 leads; body width 4.4 mm	SOT360-1	

#### **Functional diagram** 4.



# 5. Pinning information

# 5.1 Pinning



# 5.2 Pin description

### Table 2. Pin description

Symbol	Pin	Description
DIR	1	direction control
A0 to A7	2, 3, 4, 5, 6, 7, 8, 9	data input/output
GND	10	ground (0 V)
B0 to B7	18, 17, 16, 15, 14, 13, 12, 11	data input/output
ŌE	19	output enable input (active LOW)
V <sub>CC</sub>	20	supply voltage

# 6. Functional description

Table 3.	Function table <sup>[1]</sup>			
Input OE		Input/output		
OE	DIR	An	Bn	
L	L	A = B	input	
L	Н	input	B = A	
Н	Х	Z	Z	

[1] H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

# 7. Limiting values

### Table 4.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+7.0	V
VI	input voltage		[1]	-0.5	+7.0	V
Vo	output voltage	active mode	[2][3]	-0.5	V <sub>CC</sub> + 0.5	V
		power-down or 3-state mode	[2]	-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>1</sub> < 0 V		-50	-	mA
I <sub>ОК</sub>	output clamping current	V <sub>O</sub> < 0 V		-50	-	mA
I <sub>O</sub>	output current	$V_{O} = 0 V$ to $V_{CC}$		-	±50	mA
I <sub>CC</sub>	supply current			-	100	mA
I <sub>GND</sub>	ground current			-100	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$	[4]	-	500	mW

[1] If the input current ratings are observed, the minimum input voltage ratings may be exceeded.

[2] If the output current ratings are observed, the output voltage ratings may be exceeded.

[3] This value is limited to 7.0 V maximum.

[4] For TSSOP20 package: above 100 °C, the value of  $P_{tot}$  derates linearly with 10 mW/K.

# 8. Recommended operating conditions

### Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		1.8	5.5	V
VI	input voltage		0	5.5	V
Vo	output voltage	active mode	0	V <sub>CC</sub>	V
		power-down or 3-state mode	0	5.5	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
$\Delta t / \Delta V$	input transition rise and fall rate	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	50	ms/V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	-	20	ms/V
		$V_{CC}$ = 4.5 V to 5.5 V	-	1	ms/V

# 9. Static characteristics

### Table 6.Static characteristics

Voltages are referenced to GND (ground = 0 V).

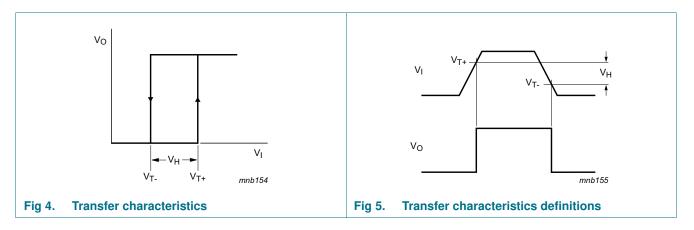
Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
V <sub>T+</sub>	positive-going	V <sub>CC</sub> = 1.8 V	-	-	1.65	-	1.65	-	1.65	V
	threshold	V <sub>CC</sub> = 2.3 V	-	-	1.85	-	1.85	-	1.85	V
	voltage	V <sub>CC</sub> = 3.0 V	-	-	2.2	-	2.2	-	2.2	V
		V <sub>CC</sub> = 4.5 V	-	-	3.15	-	3.15	-	3.15	V
		V <sub>CC</sub> = 5.5 V	-	-	3.85	-	3.85	-	3.85	V
V <sub>T-</sub> negative-goir	negative-going	V <sub>CC</sub> = 1.8 V	0.15	-	-	0.15	-	0.15	-	V
	threshold	V <sub>CC</sub> = 2.3 V	0.45	-	-	0.45	-	0.45	-	V
V	voltage	V <sub>CC</sub> = 3.0 V	0.9	-	-	0.9	-	0.9	-	V
		V <sub>CC</sub> = 4.5 V	1.35	-	-	1.35	-	1.35	-	V
		V <sub>CC</sub> = 5.5 V	1.65	-	-	1.65	-	1.65	-	V
V <sub>H</sub>	hysteresis	V <sub>CC</sub> = 1.8 V	0.15	-	1.05	0.15	1.05	0.15	1.05	V
	voltage	V <sub>CC</sub> = 2.3 V	0.2	-	1.1	0.2	1.1	0.2	1.1	V
		V <sub>CC</sub> = 3.0 V	0.3	-	1.2	0.3	1.2	0.3	1.2	V
		V <sub>CC</sub> = 4.5 V	0.4	-	1.4	0.4	1.4	0.4	1.4	V
		V <sub>CC</sub> = 5.5 V	0.5	-	1.6	0.5	1.6	0.5	1.6	V
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{T+}$ or $V_{T-}$								V
	output voltage	$I_{O} = -50 \ \mu A; V_{CC} = 1.8 \ V$	1.7	1.8	-	1.7	-	1.7	-	V
		$I_{O} = -50 \ \mu A; V_{CC} = 3.0 \ V$	2.9	3.0	-	2.9	-	2.9	-	V
		$I_{O} = -50 \ \mu A; V_{CC} = 4.5 \ V$	4.4	4.5	-	4.4	-	4.4	-	V
		$I_{O} = -8 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.48	-	V
		$I_{O} = -16 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.94	-	-	3.80	-	3.80	-	

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#### Voltages are referenced to GND (ground = 0 V). Conditions 25 °C -40 °C to +85 °C -40 °C to +125 °C Unit Symbol Parameter Min Тур Max Min Max Min Max VOL LOW-level $V_I = V_{T+} \text{ or } V_{T-}$ output voltage $I_{O} = 50 \ \mu A; V_{CC} = 1.8 \ V$ 0 0.1 0.1 0.1 V \_ \_ \_ $I_{O} = 50 \ \mu A; V_{CC} = 3.0 \ V$ 0 0.1 0.1 0.1 V --\_ $I_{O} = 50 \ \mu A; V_{CC} = 4.5 \ V$ \_ 0 0.1 \_ 0.1 \_ 0.1 V $I_{O} = 8 \text{ mA}; V_{CC} = 3.0 \text{ V}$ 0.36 v 0.44 0.44 \_ \_ \_ \_ $I_{O} = 16 \text{ mA}; V_{CC} = 4.5 \text{ V}$ 0.44 0.55 0.55 v ----OFF-state $V_{CC} = 5.5 V; V_I = V_{IH} \text{ or } V_{IL};$ ±0.25 ±2.5 \_ \_ ±2.5 μA \_ \_ loz $V_0 = GND$ to 5.5 V output current $V_{I}$ or $V_{O}$ = GND to 5.5 V; power-off 0.5 5 5 μA **I**OFF \_ \_ \_ $V_{CC} = 0 V$ leakage current $V_I = V_{CC} \text{ or } GND;$ I<sub>L</sub> input leakage ±0.1 ±1 ±1 μA \_ -\_ V<sub>CC</sub> = 0 V to 5.5 V current $V_I = V_{CC}$ or GND; $I_O = 0$ A; supply current 2 20 20 μA Icc \_ \_ \_ \_ $V_{CC} = 5.5 V$

### Static characteristics ... continued Table 6.

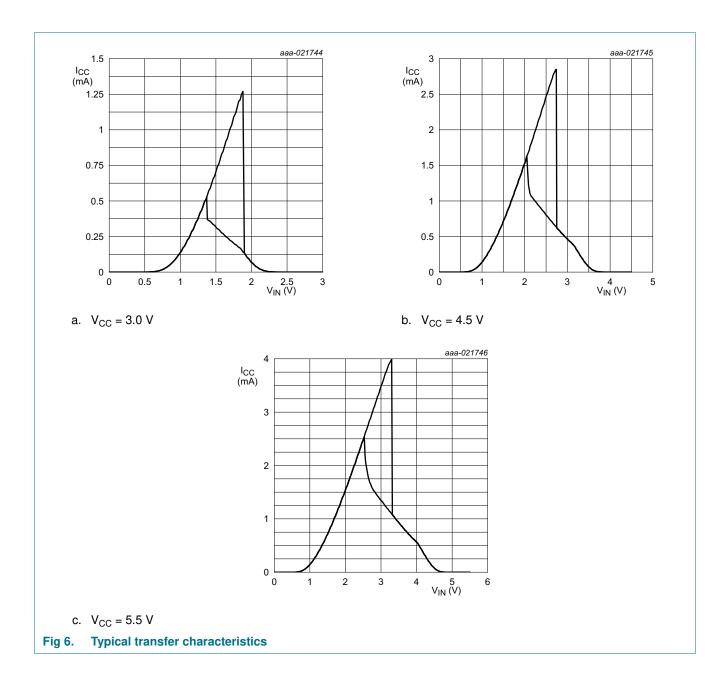
#### **Transfer characteristics waveforms** 9.1



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# **10. Dynamic characteristics**

### Table 7. Dynamic characteristics

GND = 0 V. For test circuit, see Figure 9.

Symbol	Parameter	Conditions		25 °C		–40 °C	to +85 °C	–40 °C t	o +125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Мах	
t <sub>pd</sub>	propagation delay	An to Bn or Bn to An; see [2] Figure 7								
		V <sub>CC</sub> = 2.3 V to 2.7 V								
		C <sub>L</sub> = 15 pF	-	5.3	13	1	15	1	16.3	ns
		C <sub>L</sub> = 50 pF	-	7.3	15.9	1	18	1	19.4	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V								
		C <sub>L</sub> = 15 pF	-	4.1	8.4	1	10	1	10.9	ns
		C <sub>L</sub> = 50 pF	-	5.7	11.9	1	13.5	1	14.6	ns
		$V_{CC} = 4.5 \text{ V} \text{ to } 5.5 \text{ V}$								
		C <sub>L</sub> = 15 pF	-	3.2	5.5	1	6.5	1	7	ns
		C <sub>L</sub> = 50 pF	-	4.5	7.5	1	8.5	1	9.3	ns
t <sub>en</sub>	enable time	OE to An or OE to Bn; see  [2]    Figure 8								
		V <sub>CC</sub> = 2.3 V to 2.7 V								
	C <sub>L</sub> = 15 pF	-	6.5	19.9	1	22	1	23	ns	
		C <sub>L</sub> = 50 pF	-	8.6	22.7	1	26	1	27	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$								
		C <sub>L</sub> = 15 pF	-	4.9	13.2	1	15.5	1	16	ns
		C <sub>L</sub> = 50 pF	-	6.6	16.7	1	19	1	20	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V								
		C <sub>L</sub> = 15 pF	-	3.7	8.5	1	10	1	10.5	ns
		C <sub>L</sub> = 50 pF	-	5.1	10.6	1	12	1	12.5	ns
t <sub>dis</sub>	disable time	OE to An or OE to Bn; see [2] Figure 8								
		V <sub>CC</sub> = 2.3 V to 2.7 V								
		C <sub>L</sub> = 15 pF	-	7	15.1	1	16.7	1	17.8	ns
		C <sub>L</sub> = 50 pF	-	11.6	23.1	1	25	1	26	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V								
		C <sub>L</sub> = 15 pF	-	5.7	10.5	1	12.3	1	13.2	ns
		C <sub>L</sub> = 50 pF	-	9.1	15.8	1	18	1	19	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V								
		C <sub>L</sub> = 15 pF	-	4.5	6.9	1	8	1	8.5	ns
		C <sub>L</sub> = 50 pF	-	6.7	9.7	1	11	1	11.5	ns
t <sub>sk(o)</sub>	output skew	C <sub>L</sub> = 50 pF								
	time	V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	2	-	2	-	2	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	1.5	-	1.5	-	1.5	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	-	1	-	1	-	1	ns

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Symbol Parameter		Conditions		25 °C		–40 °C to +85 °C		–40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
Cı	input capacitance		-	2	6	-	6	-	6	pF
C <sub>I/O</sub>	input/output capacitance	$V_{O} = V_{CC}$ or GND; $V_{CC} = 3.3 \text{ V}$	-	5.5	-	-	-	-	-	pF
C <sub>PD</sub>	power dissipation capacitance	$\label{eq:constraint} \begin{array}{ll} \text{per buffer;} & \underline{\mbox{[3]}}\\ C_L = 0 \mbox{ pF; } f = 10 \mbox{ MHz;}\\ V_{CC} = 5 \mbox{ V;}\\ V_I = GND \mbox{ to } V_{CC} \end{array}$	-	13.8	-	-	-	-	-	pF

# Table 7.Dynamic characteristics ... continuedGND = 0 V. For test circuit, see Figure 9.

[1] Typical values are measured at T<sub>amb</sub> = 25 °C and V<sub>CC</sub> = 2.5 V, 3.3 V, and 5 V respectively, unless otherwise specified.

- t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>.
  t<sub>en</sub> is the same as t<sub>PZL</sub> and t<sub>PZH</sub>.
  t<sub>dis</sub> is the same as t<sub>PLZ</sub> and t<sub>PHZ</sub>.
- [3]  $C_{PD}$  is used to determine the dynamic power dissipation  $P_D$  ( $\mu$ W).
  - $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

 $f_o = output frequency in MHz;$ 

 $C_L$  = output load capacitance in pF;

 $V_{CC}$  = supply voltage in Volts.

### Table 8.Noise characteristics

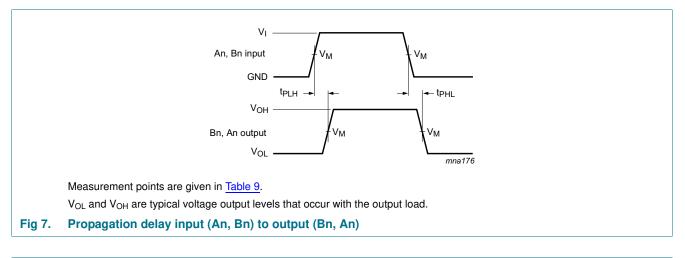
### GND = 0 V. For test circuit, see Figure 9.

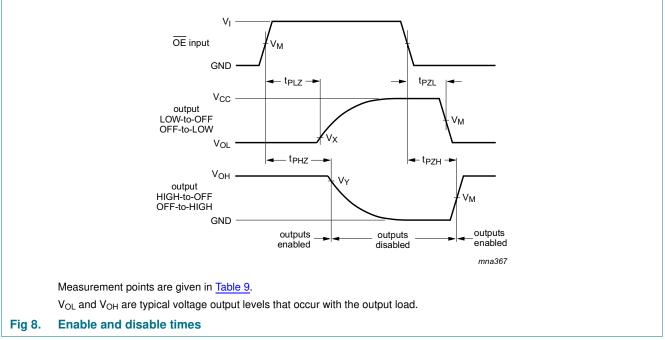
Symbol	Parameter	Conditions	т	<sub>amb</sub> = 25 °C	)	Unit
			Min	Тур	Max	V V V V V V V V V V V V V
$V_{\rm CC} = 3.3$	<sup>3</sup> V; C <sub>L</sub> = 50 pF	I				
V <sub>OL(p)</sub>	LOW-level output voltage (peak)		-	0.3	0.8	V
V <sub>OL(v)</sub>	LOW-level output voltage (valley)		-0.8	-0.2	-	V
V <sub>OH(v)</sub>	HIGH-level output voltage (valley)		-	2.9	-	V
V <sub>IH(AC)</sub>	AC HIGH-level input voltage	dynamic	2.31	-	-	V
V <sub>IL(AC)</sub>	AC LOW-level input voltage	dynamic	-	-	0.99	V
$V_{\rm CC} = 5.0$	) V; C <sub>L</sub> = 50 pF	I				
V <sub>OL(p)</sub>	LOW-level output voltage (peak)		-	0.6	1.5	V
V <sub>OL(v)</sub>	LOW-level output voltage (valley)		-1.5	-0.6	-	V
V <sub>OH(v)</sub>	HIGH-level output voltage (valley)		-	4.0	-	V
V <sub>IH(AC)</sub>	AC HIGH-level input voltage	dynamic	3.5	-	-	V
V <sub>IL(AC)</sub>	AC LOW-level input voltage	dynamic	-	-	1.5	V

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# 11. Waveforms





### Table 9.Measurement points

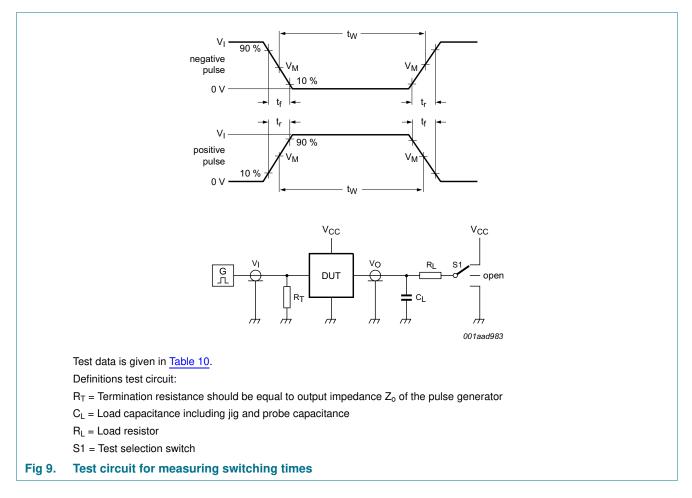
Input	Output		
V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>
0.5V <sub>CC</sub>	0.5V <sub>CC</sub>	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> – 0.3 V

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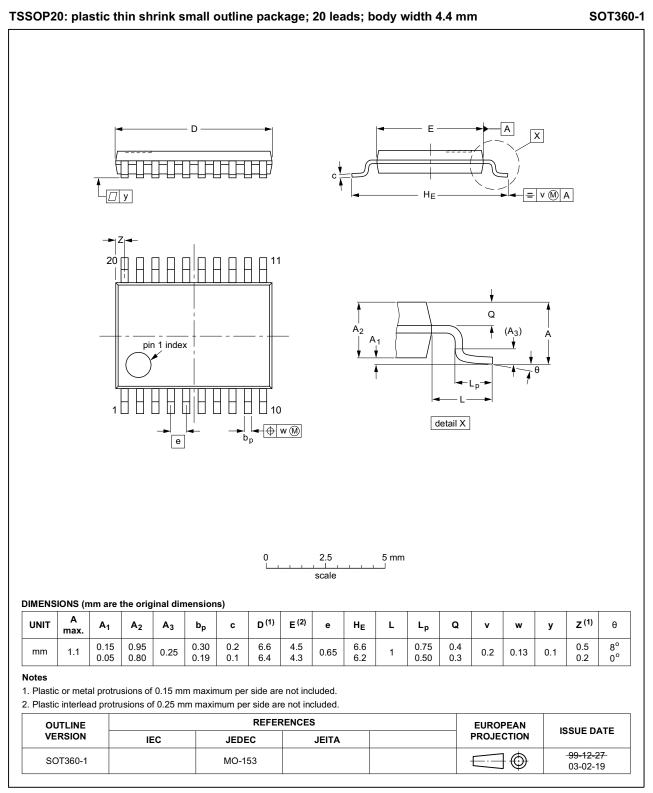
### Octal bus transceiver; 3-state



### Table 10. Test data

Input		Load		S1 position		
VI	t <sub>r</sub> , t <sub>f</sub>	CL	RL	t <sub>PHL</sub> , t <sub>PLH</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
GND to $V_{CC}$	3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V <sub>CC</sub>

# 12. Package outline



### Fig 10. Package outline SOT360-1 (TSSOP20)

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# **13. Abbreviations**

Table 11. Abbreviations					
Acronym	Description				
CDM	Charge Device Model				
DUT	Device Under Test				
ESD	ElectroStatic Discharge				
HBM	Human Body Model				
MM	Machine Model				
TTL	Transistor-Transistor Logic				

# 14. Revision history

### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHCV245A v.2	20161108	Product data sheet	-	74AHCV245A v.1
Modifications:	Type number 74AHCV245ABQ removed.			
74AHCV245A v.1	20160610	Product data sheet	-	-

# **15. Legal information**

### 15.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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### **Nexperia**

# 74AHCV245A

### Octal bus transceiver; 3-state

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74AHCV245A

**Product data sheet** 

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### Octal bus transceiver; 3-state

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